

Features

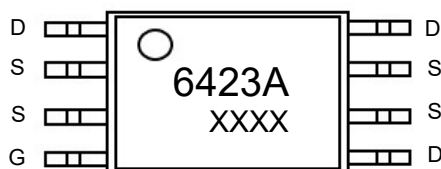
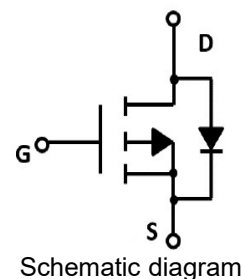
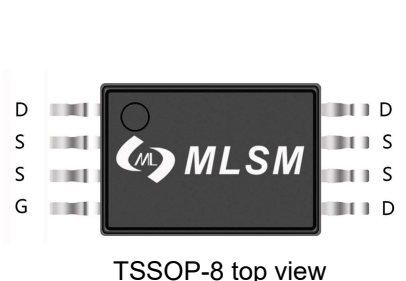
- High power and current handling capability
- Lead free product is acquired
- Surface mount package

Product Summary

V_{DS}	$R_{DS(ON)}$ MAX	I_D MAX
-12V	8.5mΩ@-4.5V	-9.5A
	10.6mΩ@-2.5V	

Application

- Power management
- Portable equipment



6423A: Device code
XXXX: Code



Halogen-Free

Absolute Maximum Ratings (TA=25°C unless otherwise noted)

Symbol	Parameter	Rating	Unit
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Common Ratings (TC=25°C Unless Otherwise Noted)

V _{DS}	Drain-Source Breakdown Voltage		-12	V
V _{GS}	Gate-Source Voltage		±8	V
T _J	Maximum Junction Temperature		150	°C
T _{STG}	Storage Temperature Range		-50 to 155	°C
I _S	Diode Continuous Forward Current	Tc=25°C	-9.5	A

Mounted on Large Heat Sink

I_{DM}	Pulse Drain Current Tested	$T_c=25^\circ\text{C}$	-30	A
I_D	Continuous Drain Current	$T_c=25^\circ\text{C}$	-9.5	A
P_D	Maximum Power Dissipation	$T_c=25^\circ\text{C}$	1.5	W
$R_{\theta JA}$	Thermal Resistance Junction-Ambient		120	°C/W

Ordering Information (Example)

Type	Package	Marking	Minimum Package(pcs)	Inner Box Quantity(pcs)	Outer Carton Quantity(pcs)	Delivery Mode
MLSS6423A	TSSOP-8	6423A	3,000	6,000	42,000	13"reel



Electrical Characteristics (T _J =25°C unless otherwise noted)						
Symbol	Parameter	Condition	Min	Typ	Max	Unit
Static Electrical Characteristics @ T _J = 25°C (unless otherwise stated)						
BV _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =-250μA	-12	--	--	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =-12V, V _{GS} =0V	--	--	-1	μA
I _{GSS}	Gate-Body Leakage Current	V _{GS} =±8V, V _{DS} =0V	--	--	±100	nA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =-250μA	-0.4	--	-0.8	V
R _{DS(on)}	Drain-Source On-State Resistance	V _{GS} =-4.5V, I _D =-9.5A	--	6.8	8.5	mΩ
		V _{GS} =-2.5V, I _D =-8.5A	--	8.5	10.6	mΩ
Dynamic Electrical Characteristics @ T _J = 25°C (unless otherwise stated)						
C _{ISS}	Input Capacitance	V _{DS} =-6V, V _{GS} =0V, f=1MHz	--	5750	--	pF
C _{OSS}	Output Capacitance		--	2130	--	pF
C _{RSS}	Reverse Transfer Capacitance		--	1710	--	pF
Switching Characteristics						
Q _g	Total Gate Charge	V _{DS} =-6V, I _D =-9.5A, V _{GS} =-5V	--	74	--	nC
Q _{gs}	Gate Source Charge		--	9	--	nC
Q _{gd}	Gate Drain Charge		--	19	--	nC
t _{d(on)}	Turn-on Delay Time	V _{DD} =-6V, I _D =-1A, V _{GS} =-4.5V, R _G =6Ω	--	50	--	nS
t _r	Turn-on Rise Time		--	75	--	nS
t _{d(off)}	Turn-Off Delay Time		--	270	--	nS
t _f	Turn-Off Fall Time		--	200	--	nS
Source- Drain Diode Characteristics						
V _{SD}	Forward on voltage	T _J =25°C, I _S =-9.5A	--	-0.8	-1.2	V

Typical Operating Characteristics

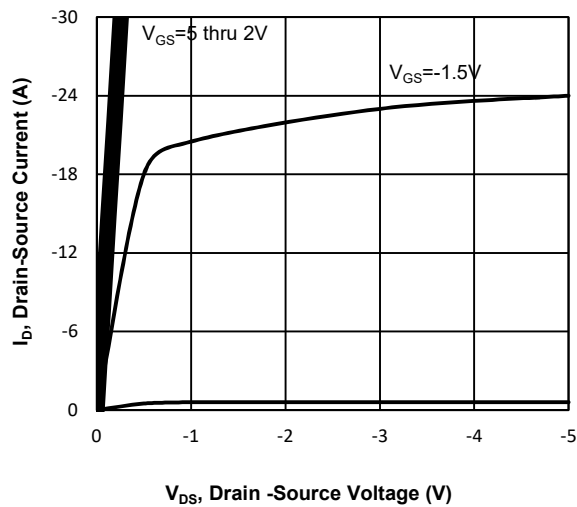


Fig1. Typical Output Characteristics

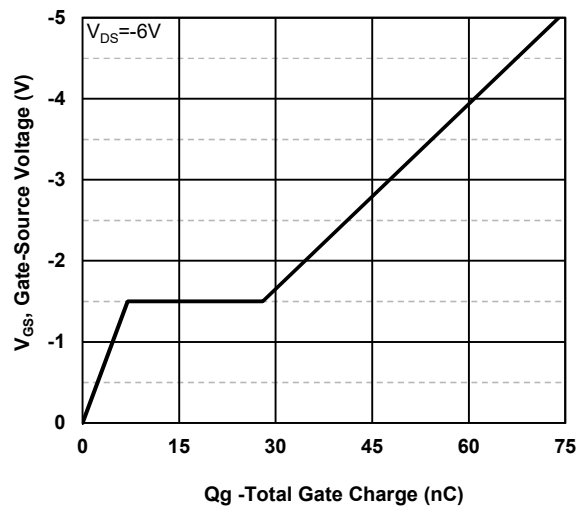


Fig2. Typical Gate Charge Vs. Gate-Source Voltage

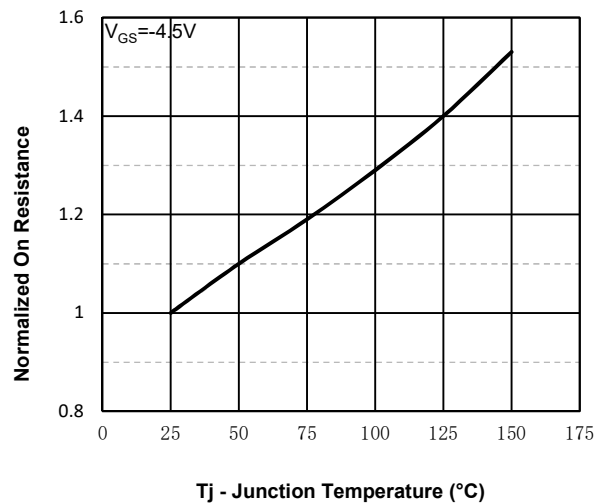


Fig3. Normalized On-Resistance Vs. Temperature

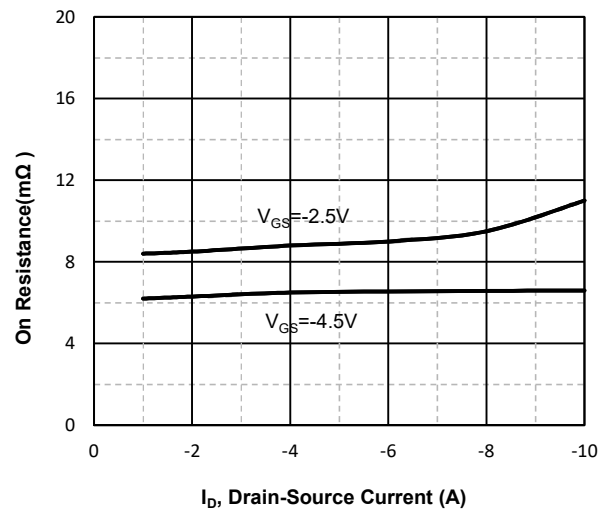


Fig4. On-Resistance Vs. Drain-Source Current

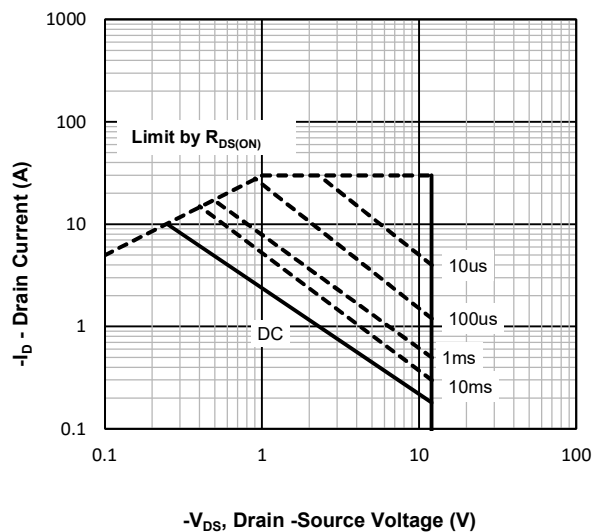


Fig5. Maximum Safe Operating Area

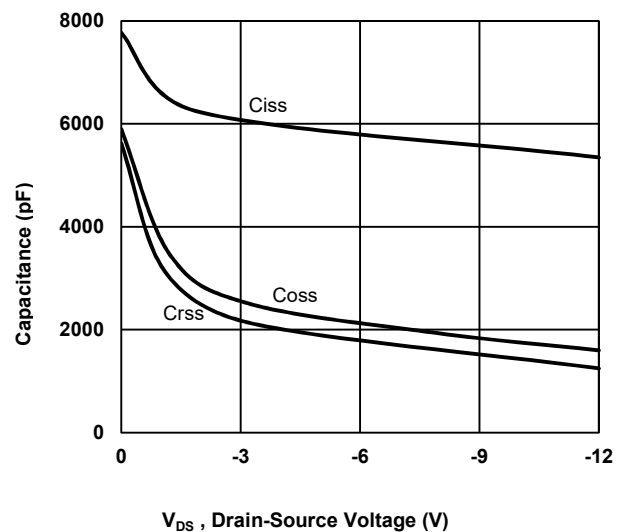
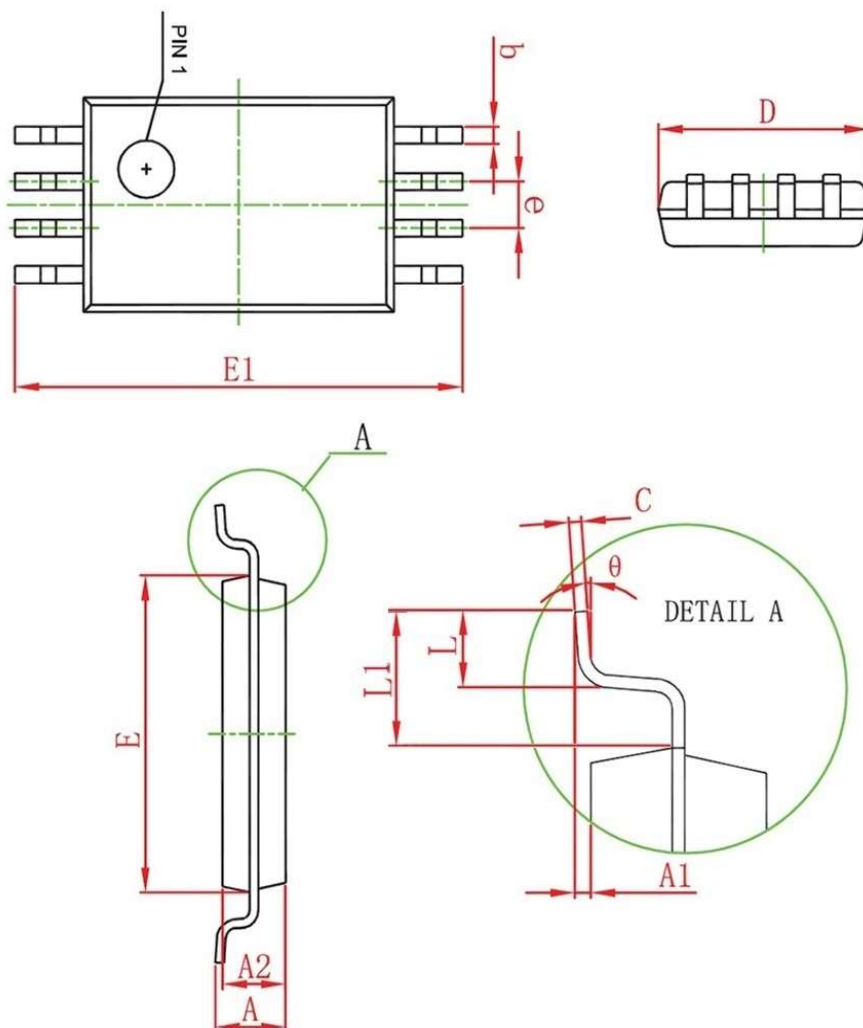


Fig6 Typical Capacitance Vs. Drain-Source Voltage

TSSOP-8 Package information



Symbol	Dimensions in Millimeters(mm)		Dimensions in Inches	
	Min	Max	Min	Max
A	1.000	1.200	0.039	0.047
A1	0.020	0.180	0.000	0.007
A2	0.900	1.100	0.035	0.043
b	0.170	0.270	0.006	0.010
c	0.122	0.132	0.004	0.005
D	2.870	3.070	0.112	0.120
e	0.65BSC		0.025BSC	
E	4.300	4.500	0.169	0.177
E1	6.200	6.600	0.244	0.259
L	0.400	0.800	0.015	0.031
L1	1.00BSC		0.039BSC	
Ø1	0.500	0.700	0.001	0.027
θ	0°	10°	0°	10°