

Features

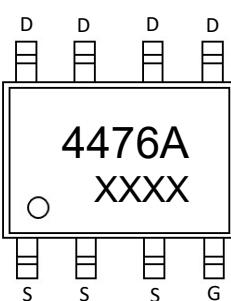
- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- Green Device Available

Product Summary

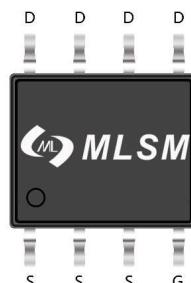
V_{DS}	$R_{DS(ON)} \text{ MAX}$	$I_D \text{ MAX}$
30V	7.7mΩ@10V	15A
	10.8mΩ@4.5V	

Application

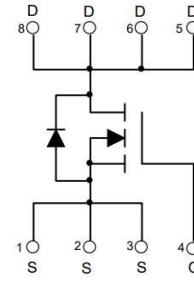
- High Frequency Point-of-Load Synchronous Buck Converter for MB/NB/UMPC/VGA
- Networking DC-DC Power System
- Load Switch



4476A: Device code
XXXX : Code



SOP-8 top view



Schematic diagram

Marking and pin assignment



Halogen-Free

Absolute Maximum Ratings (TA=25°C unless otherwise noted)

Symbol	Parameter	Rating	Unit	
Common Ratings (TC=25°C Unless Otherwise Noted)				
V_{DS}	Drain-Source Breakdown Voltage	30	V	
V_{GS}	Gate-Source Voltage	±20	V	
T_J	Maximum Junction Temperature	150	°C	
T_{STG}	Storage Temperature Range	-55 to 150	°C	
I_S	Diode Continuous Forward Current	15	A	
Mounted on Large Heat Sink				
I_{DM}	Pulse Drain Current Tested	Tc=25°C	73	A
I_D	Continuous Drain Current	Tc=25°C	15	A
P_D	Maximum Power Dissipation	Tc=25°C	3	W
$R_{θJA}$	Thermal Resistance Junction-Ambient		40 °C/W	

Ordering Information (Example)

Type	Package	Marking	Minimum Package(pcs)	Inner Box Quantity(pcs)	Outer Carton Quantity(pcs)	Delivery Mode
MLSQ4476A	SOP-8	4476A	3,000	6,000	42,000	13" reel

Electrical Characteristics (T_J=25°C unless otherwise noted)

Symbol	Parameter	Condition	Min	Typ	Max	Unit
Static Electrical Characteristics @ T_J = 25°C (unless otherwise stated)						
BV _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250μA	30	--	-	V
I _{BS}	Zero Gate Voltage Drain Current	V _{DS} =30V, V _{GS} =0V	--	--	1	μA
I _{GSS}	Gate-Body Leakage Current	V _{GS} =±20V, V _{DS} =0V	--	--	±100	nA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250μA	1.0	--	2.5	V
R _{DS(on)}	Drain-Source On-State Resistance	V _{GS} =10V, I _D =15A	--	6.4	7.7	mΩ
		V _{GS} =4.5V, I _D =12A	--	8.6	10.8	mΩ

Dynamic Electrical Characteristics @ T_J = 25°C (unless otherwise stated)

C _{ISS}	Input Capacitance	V _{DS} =15V, V _{GS} =0V, f=1MHz	--	1300	-	pF
C _{OSS}	Output Capacitance		--	180	-	pF
C _{RSS}	Reverse Transfer Capacitance		--	110	-	pF

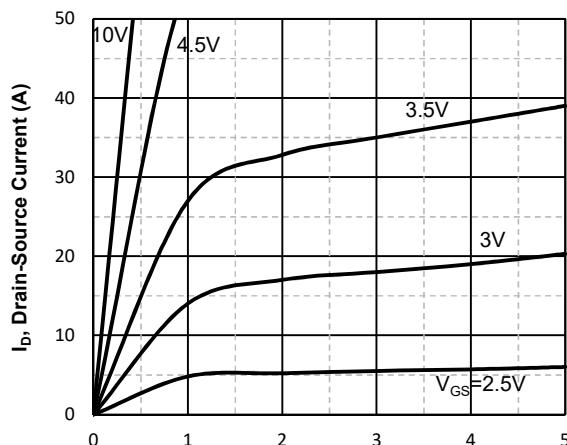
Switching Characteristics

Q _g	Total Gate Charge	V _{DS} =20V, I _D =15A, V _{GS} =4.5V	--	14	-	nC
Q _{gs}	Gate Source Charge		--	3.5	-	nC
Q _{gd}	Gate Drain Charge		--	7	-	nC
t _{d(on)}	Turn-on Delay Time	V _{DD} =12V, I _D =15A, V _{GS} =10V, R _G =3.3Ω	--	5	-	nS
t _r	Turn-on Rise Time		--	12	-	nS
t _{d(off)}	Turn-Off Delay Time		--	27	-	nS
t _f	Turn-Off Fall Time		--	10	-	nS

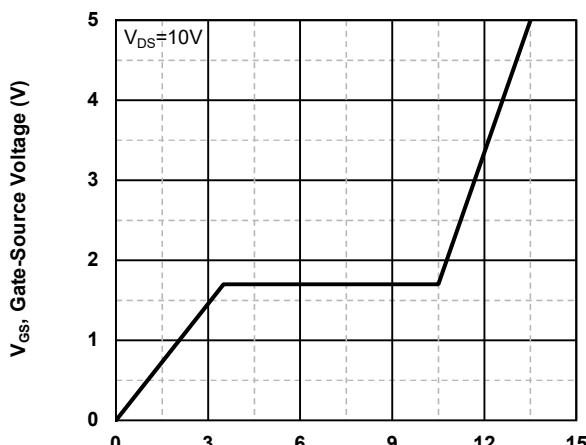
Source-Drain Diode Characteristics

V _{SD}	Forward on voltage	T _j =25°C, I _S =15A	--	--	1.2	V
-----------------	--------------------	---	----	----	-----	---

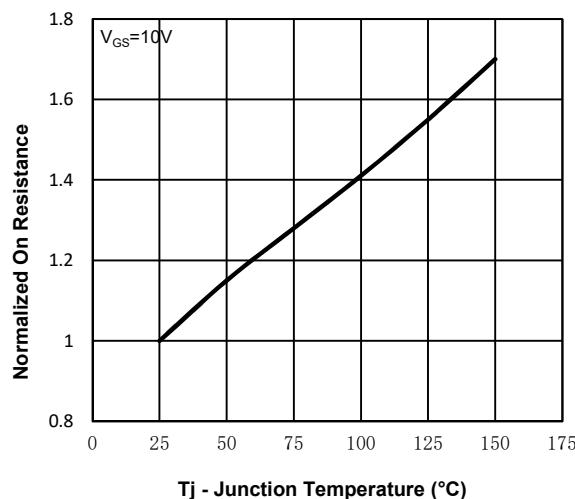
Typical Operating Characteristics



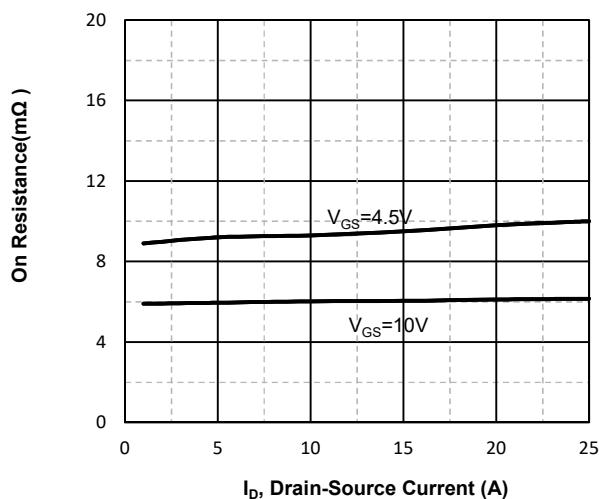
V_{DS} , Drain -Source Voltage (V)
Fig1. Typical Output Characteristics



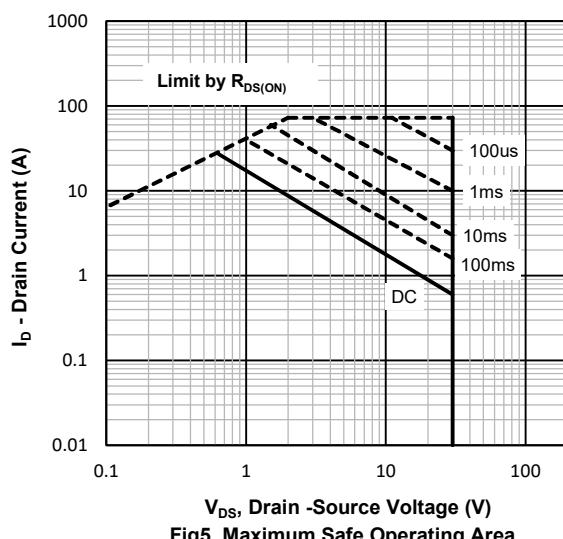
V_{GS} , Gate-Source Voltage (V)
 Q_g -Total Gate Charge (nC)
Fig2. Typical Gate Charge Vs.Gate-Source Voltage



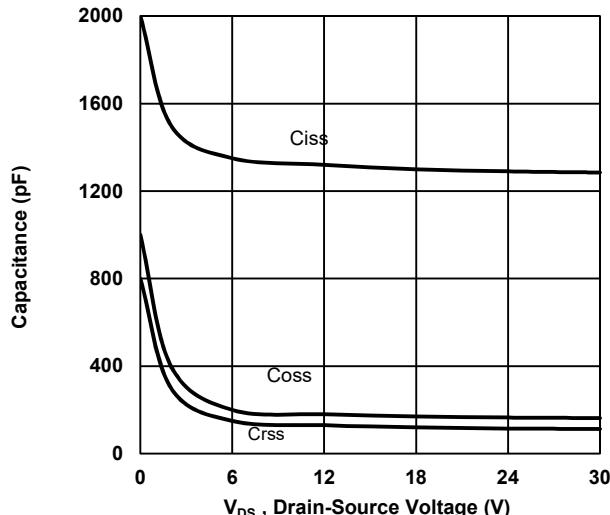
$V_{GS}=10V$
 T_j - Junction Temperature (°C)
Fig3. Normalized On-Resistance Vs. Temperature



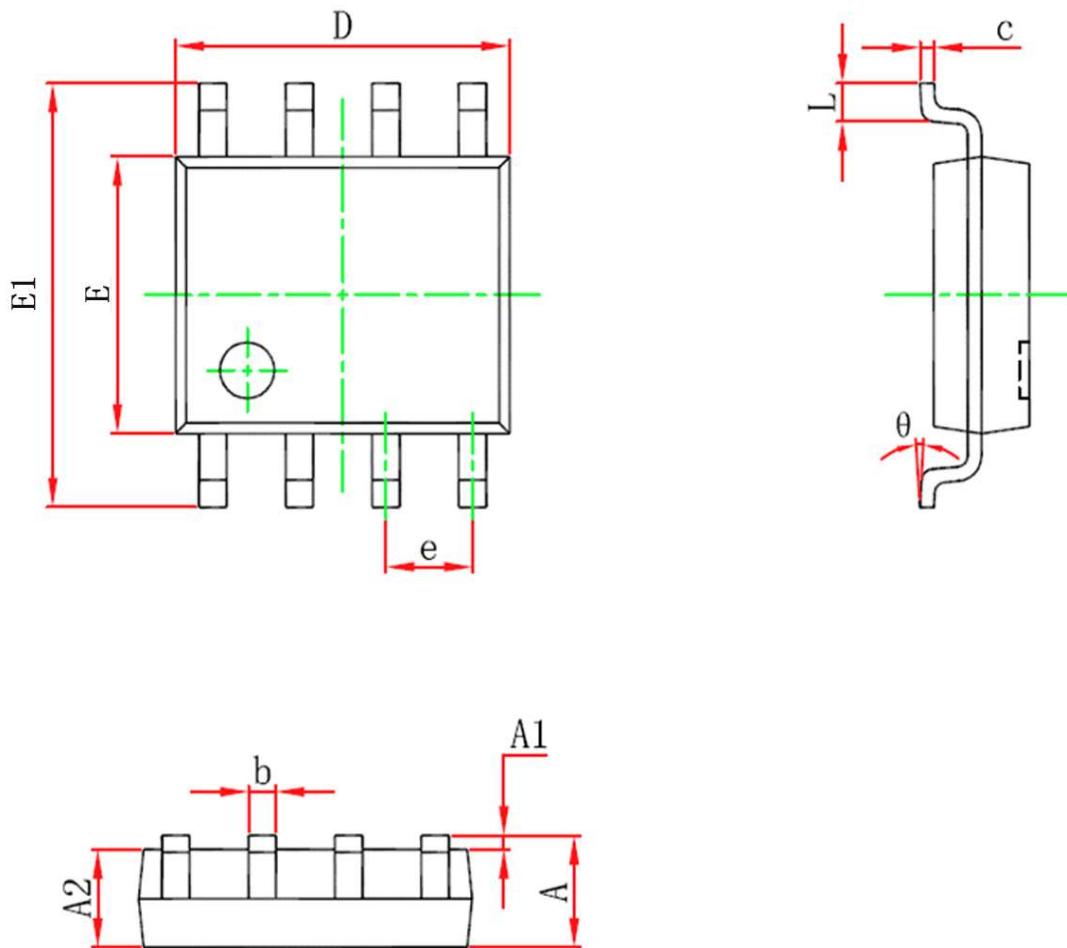
$V_{GS}=4.5V$
 $V_{GS}=10V$
 I_D , Drain-Source Current (A)
Fig4. On-Resistance Vs. Drain-Source Current



V_{DS} , Drain -Source Voltage (V)
Fig5. Maximum Safe Operating Area



V_{DS} , Drain-Source Voltage (V)
Fig6 Typical Capacitance Vs.Drain-Source

SOP-8 Package information


Symbol	Dimensions in Millimeters(mm)		Dimensions In Inches	
	Min	Max	Min	Max
A	1.450	1.750	0.057	0.068
A1	0.100	0.250	0.003	0.009
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.012	0.020
c	0.170	0.250	0.006	0.009
D	4.700	5.100	0.185	0.200
e	1.270(BSC)		0.050(BSC)	
E	3.800	4.000	0.149	0.157
E1	5.800	6.200	0.228	0.244
L	0.400	1.270	0.015	0.050
θ	0°	8°	0°	8°