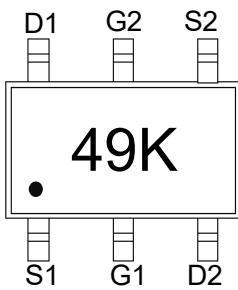


## Features

- Surface Mount Package
- Low  $R_{DS(on)}$
- Operated at Low Logic Level Gate Drive
- ESD Protected Gate

## Application

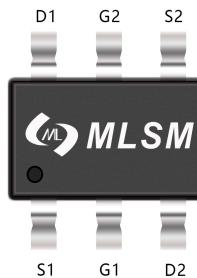
- Load/ Power Switching
- Interfacing Switching
- Battery Management for Ultra Small Portable Electronics
- Logic Level Shift



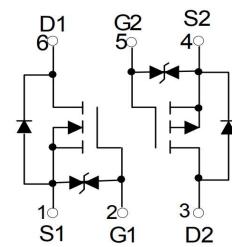
49K: Device code

## Product Summary

$V_{DS}$	$R_{DS(ON)}\text{ MAX}$	$I_D\text{ MAX}$
20V	380mΩ@4.5V	0.75A
	450mΩ@2.5V	
-20V	520mΩ@-4.5V	-0.66A
	700mΩ@-2.5V	



SOT-363 top view



Schematic diagram



Pb-Free



RoHS



Halogen-Free

Marking and pin assignment

## Absolute Maximum Ratings (TA=25°C unless otherwise noted)

Symbol	Parameter	N-Channel	P-Channel	Unit
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## Common Ratings (TC=25°C Unless Otherwise Noted)

$V_{DS}$	Drain-Source Breakdown Voltage	20	-20	V
$V_{GS}$	Gate-Source Voltage	$\pm 12$	$\pm 12$	V
$T_J$	Maximum Junction Temperature	150	150	°C
$T_{STG}$	Storage Temperature Range	-55 to 150	-55 to 150	°C
$I_S$	Diode Continuous Forward Current	Tc=25°C 0.75	-0.66	A

## Mounted on Large Heat Sink

$I_{DM}$	Pulse Drain Current Tested	Tc=25°C 1.8	-1.2	A
$I_D$	Continuous Drain Current	Tc=25°C 0.75	-0.66	A
$R_{θJA}$	Thermal Resistance Junction-Ambient	883	883	°C/W
$E_{SD}$	Gate-Source ESD Rating (HBM, Method 3015)	2000	-2000	V

## Ordering Information (Example)

Type	Package	Marking	Minimum Package(pcs)	Inner Box Quantity(pcs)	Outer Carton Quantity(pcs)	Delivery Mode
MLS3439KDW	SOT-363	49K	3,000	45,000	180,000	7"reel

**N-Ch Electrical Characteristics (TJ=25°C unless otherwise noted)**

Symbol	Parameter	Condition	Min	Typ	Max	Unit
<b>Static Electrical Characteristics @ TJ = 25°C (unless otherwise stated)</b>						
$BV_{(BR)DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	20	--	--	V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS}=20V, V_{GS}=0V$	--	--	1	$\mu A$
$I_{GSS}$	Gate-Body Leakage Current	$V_{GS}=\pm 10V, V_{DS}=0V$	--	--	$\pm 20$	$\mu A$
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	0.35	0.7	1.1	V
$R_{DS(on)}$	Drain-Source On-State Resistance	$V_{GS}=4.5V, I_D=0.65A$	--	100	380	$m\Omega$
		$V_{GS}=2.5V, I_D=0.55A$	--	135	450	$m\Omega$
		$V_{GS}=1.8V, I_D=0.45A$	--	200	800	$m\Omega$

**Dynamic Electrical Characteristics @ TJ = 25°C (unless otherwise stated)**

$C_{ISS}$	Input Capacitance	$V_{DS}=10V, V_{GS}=0V, f=1MHz$	--	33	--	pF
$C_{OSS}$	Output Capacitance		--	21	--	pF
$C_{RSS}$	Reverse Transfer Capacitance		--	10	--	pF

**Switching Characteristics**

$Q_g$	Total Gate Charge	$V_{DS}=10V, I_D=0.75A, V_{GS}=4.5V$	--	0.8	--	nC
$Q_{gs}$	Gate Source Charge		--	0.3	--	nC
$Q_{gd}$	Gate Drain Charge		--	0.17	--	nC
$t_{d(on)}$	Turn-on Delay Time	$V_{DD}=10V, I_D=0.75A, V_{GS}=4.5V, R_G=10\Omega$	--	4.2	--	nS
$t_r$	Turn-on Rise Time		--	19.1	--	nS
$t_{d(off)}$	Turn-Off Delay Time		--	10.3	--	nS
$t_f$	Turn-Off Fall Time		--	24	--	nS

**Source- Drain Diode Characteristics**

$V_{SD}$	Forward on voltage	$T_j=25^\circ C, I_s=0.75A$	--	--	1.2	V
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<b>P-Ch Electrical Characteristics (TJ=25°C unless otherwise noted)</b>						
<b>Symbol</b>	<b>Parameter</b>	<b>Condition</b>	<b>Min</b>	<b>Typ</b>	<b>Max</b>	<b>Unit</b>
<b>Static Electrical Characteristics @ TJ = 25°C (unless otherwise stated)</b>						
$BV_{(BR)DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=-250\mu A$	-20	--	--	V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS}=-20V, V_{GS}=0V$	--	--	-1	$\mu A$
$I_{GSS}$	Gate-Body Leakage Current	$V_{GS}=\pm 10V, V_{DS}=0V$	--	--	$\pm 20$	$\mu A$
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=-250\mu A$	-0.35	-0.65	-1.1	V
$R_{DS(on)}$	Drain-Source On-State Resistance	$V_{GS}=-4.5V, I_D=-1.0A$	--	380	520	$m\Omega$
		$V_{GS}=-2.5V, I_D=-0.8A$	--	520	700	$m\Omega$
		$V_{GS}=-1.8V, I_D=-0.5A$	--	750	--	$m\Omega$
<b>Dynamic Electrical Characteristics @ TJ = 25°C (unless otherwise stated)</b>						
$C_{iss}$	Input Capacitance	$V_{DS}=-10V, V_{GS}=0V, f=1MHz$	--	115	--	pF
$C_{oss}$	Output Capacitance		--	15	--	pF
$C_{rss}$	Reverse Transfer Capacitance		--	8.8	--	pF
<b>Switching Characteristics</b>						
$Q_g$	Total Gate Charge	$V_{DD}=-10V, I_D=-0.66A, V_{GS}=-4.5V$	--	1.25	--	nC
$Q_{gs}$	Gate Source Charge		--	0.35	--	nC
$Q_{gd}$	Gate Drain Charge		--	0.29	--	nC
$t_{d(on)}$	Turn-on Delay Time	$V_{DS}=-10V, I_D=-0.66A, V_{GS}=-4.5V, R_G=10\Omega$	--	9	--	nS
$t_r$	Turn-on Rise Time		--	5.5	--	nS
$t_{d(off)}$	Turn-Off Delay Time		--	30.8	--	nS
$t_f$	Turn-Off Fall Time		--	20	--	nS
<b>Source- Drain Diode Characteristics</b>						
$V_{SD}$	Forward on voltage	$T_J=25^\circ C, I_S=-0.66A$	--	--	-1.2	V

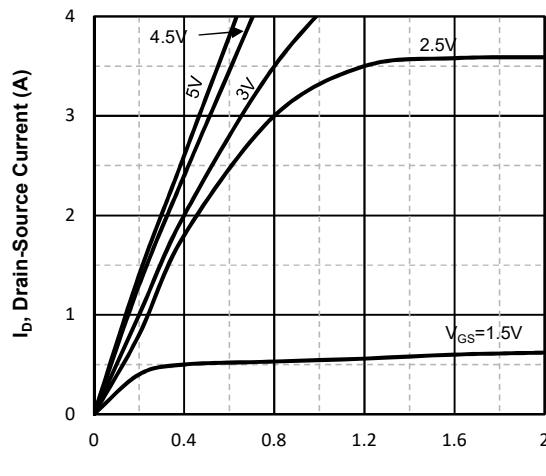
**N-Ch Typical Operating Characteristics**


Fig1. Typical Output Characteristics

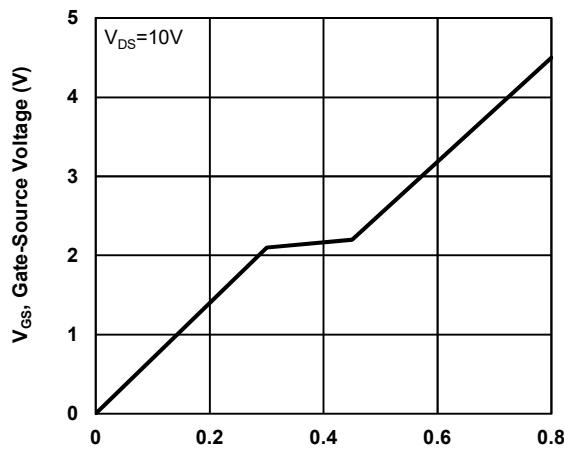


Fig2. Typical Gate Charge Vs.Gate-Source Voltage

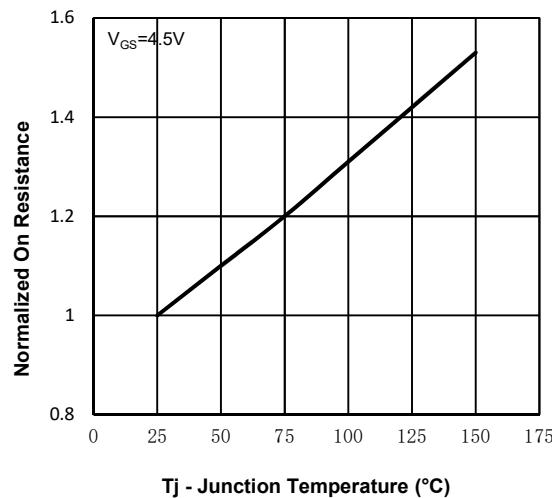


Fig3. Normalized On-Resistance Vs. Temperature

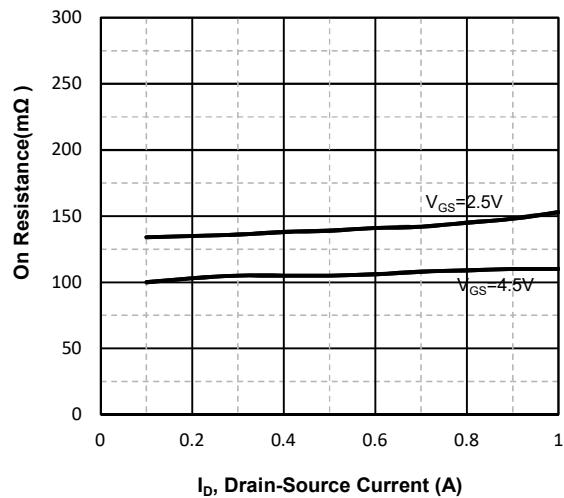


Fig4. On-Resistance Vs. Drain-Source Current

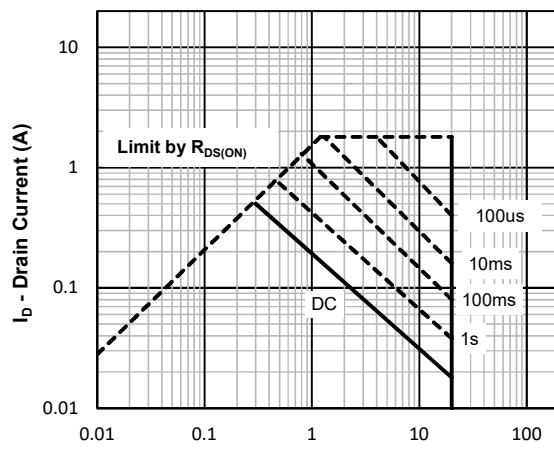


Fig5. Maximum Safe Operating Area

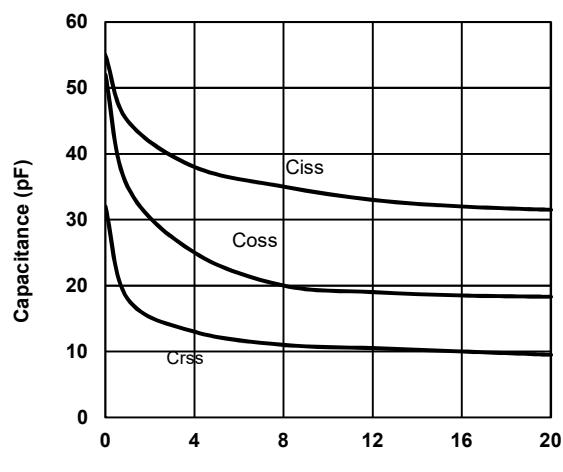


Fig6 Typical Capacitance Vs.Drain-Source

### P-Ch Typical Operating Characteristics

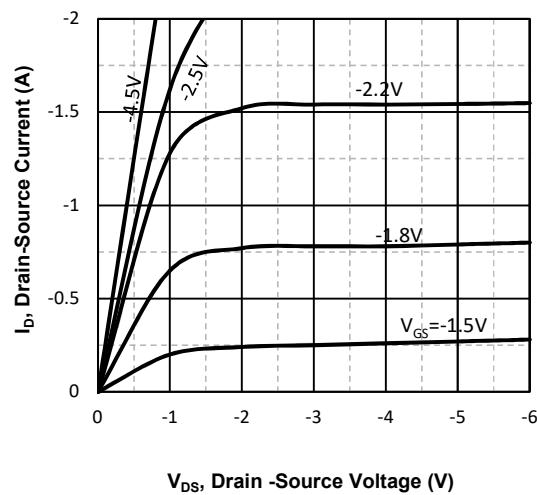


Fig7. Typical Output Characteristics

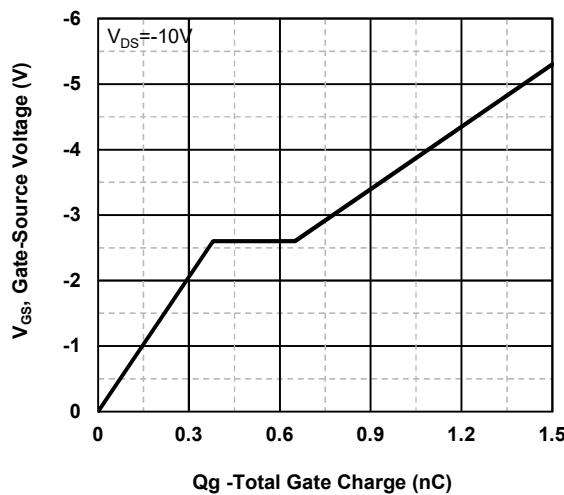


Fig8. Typical Gate Charge Vs. Gate-Source Voltage

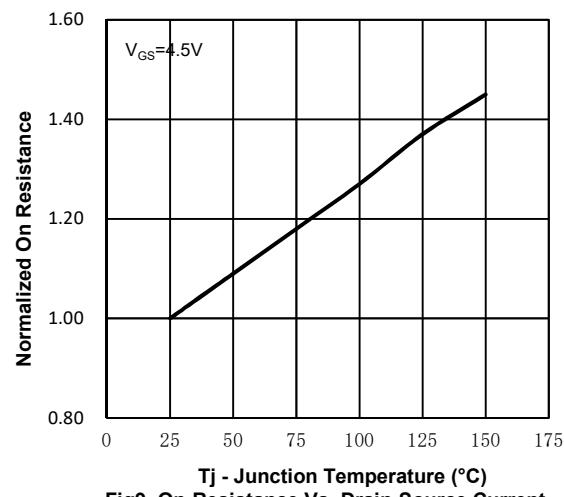


Fig9. On-Resistance Vs. Drain-Source Current

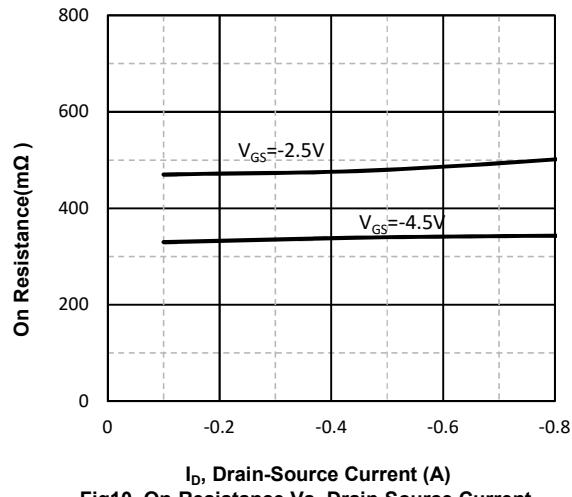


Fig10. On-Resistance Vs. Drain-Source Current

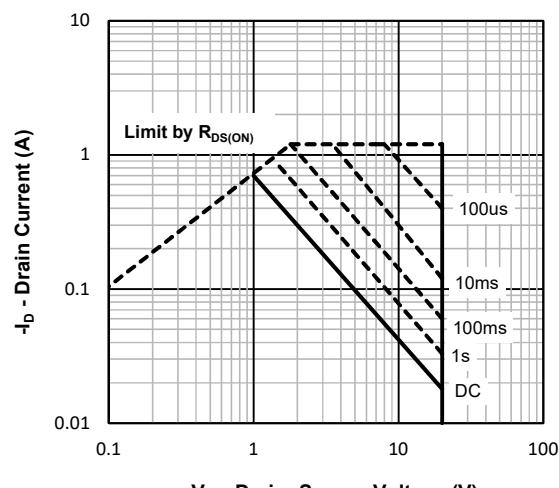


Fig11. Maximum Safe Operating Area

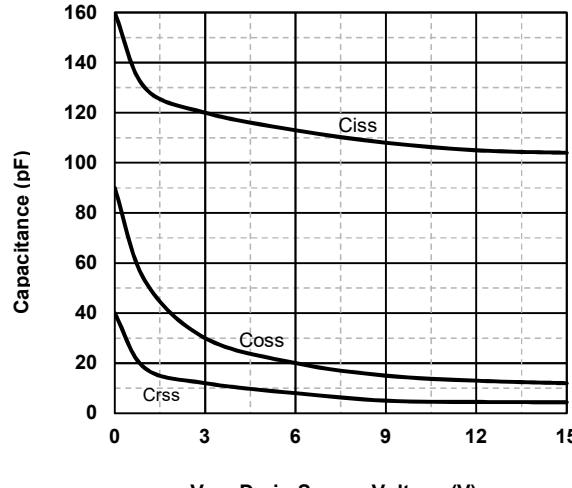
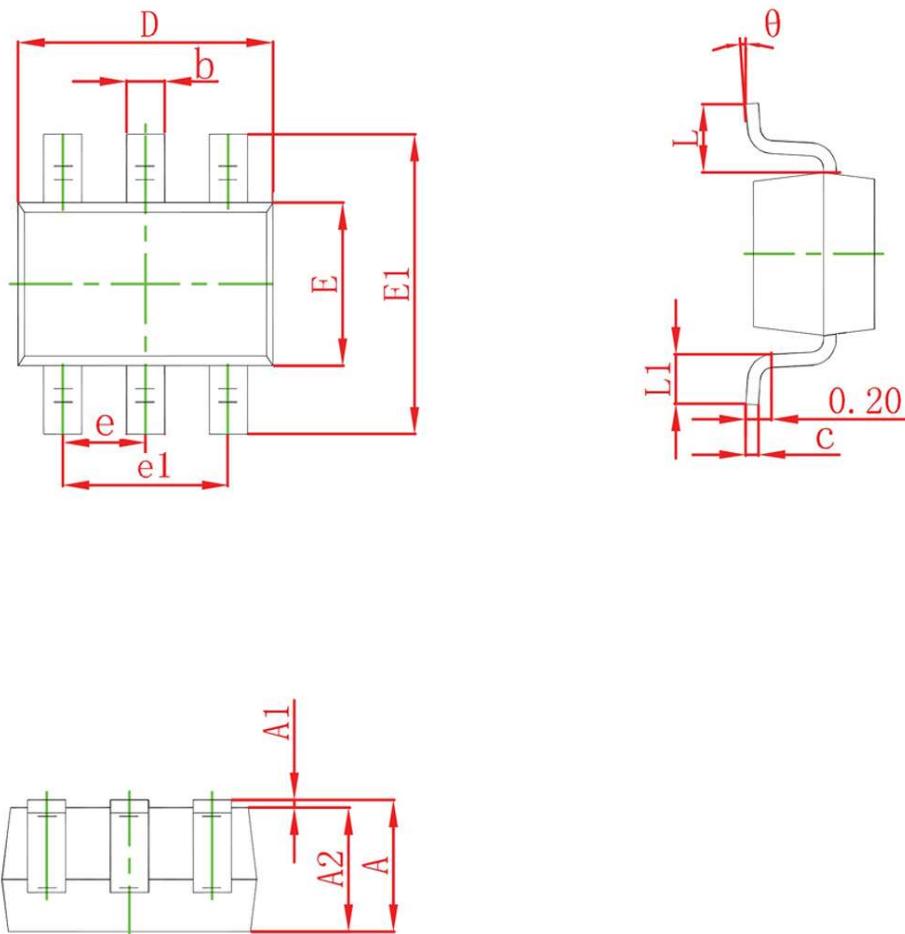


Fig12. Typical Capacitance Vs. Drain-Source Voltage

## SOT-363 Package information



Symbol	Dimensions in Millimeters(mm)		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.100	0.035	0.043
A1	0.000	0.100	0.000	0.004
A2	0.900	1.000	0.035	0.039
b	0.150	0.350	0.006	0.014
c	0.100	0.150	0.004	0.006
D	2.000	2.200	0.079	0.087
E	1.150	1.350	0.045	0.053
E1	2.150	2.400	0.085	0.094
e	0.650TYP		0.026TYP	
e1	1.200	1.400	0.047	0.055
L	0.525REF		0.021REF	
L1	0.260	0.460	0.010	0.018
theta	0°	8°	0°	8°