

## Features

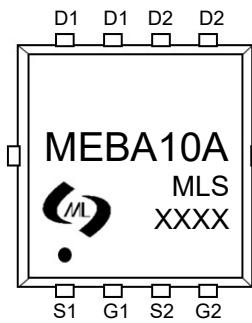
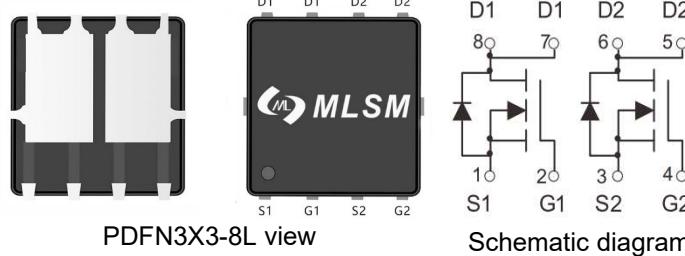
- Super high density cell design for extremely low  $R_{DS(ON)}$
- Exceptional on-resistance and maximum DC current capability

## Product Summary

$V_{DS}$	$R_{DS(ON)} \text{ MAX}$	$I_D \text{ MAX}$
100V	115mΩ@10V	10A
	130mΩ@4.5V	

## Application

- Power Management in Note book
- DC/DC Converter
- Load Switch
- LCD Display inverter



Marking and pin assignment



Halogen-Free

## Absolute Maximum Ratings (TA=25°C unless otherwise noted)

Symbol	Parameter	Rating	Unit
<b>Common Ratings (TC=25°C Unless Otherwise Noted)</b>			
$V_{DS}$	Drain-Source Breakdown Voltage	100	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$T_J$	Maximum Junction Temperature	150	°C
$T_{STG}$	Storage Temperature Range	-50 to 155	°C
$I_S$	Diode Continuous Forward Current	10	A
<b>Mounted on Large Heat Sink</b>			
$I_{DM}$	Pulse Drain Current Tested	43	A
$I_D$	Continuous Drain Current	Tc=25°C 10	A
$P_D$	Maximum Power Dissipation	Tc=25°C 3	W
$R_{θJA}$	Thermal Resistance Junction-Ambient	40	°C/W

## Ordering Information (Example)

Type	Package	Marking	Minimum Package(pcs)	Inner Box Quantity(pcs)	Outer Carton Quantity(pcs)	Delivery Mode
MEBA10A	PDFN3X3-8L	MEBA10A	5,000	10,000	70,000	13"reel



Electrical Characteristics (TJ=25°C unless otherwise noted)						
Symbol	Parameter	Condition	Min	Typ	Max	Unit
<b>Static Electrical Characteristics @ TJ = 25°C (unless otherwise stated)</b>						
BV <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	100	--	--	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =100V, V <sub>GS</sub> =0V	--	--	1.0	μA
I <sub>GSS</sub>	Gate-Body Leakage Current	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	--	--	±100	nA
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	1.0	1.9	2.5	V
R <sub>DS(on)</sub>	Drain-Source On-State Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =10A	--	85	115	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =8A	--	100	130	mΩ
<b>Dynamic Electrical Characteristics @ TJ = 25°C (unless otherwise stated)</b>						
C <sub>ISS</sub>	Input Capacitance	V <sub>DS</sub> =50V, V <sub>GS</sub> =0V, f=1MHz	--	1980	--	pF
C <sub>OSS</sub>	Output Capacitance		--	380	--	pF
C <sub>RSS</sub>	Reverse Transfer Capacitance		--	250	--	pF
<b>Switching Characteristics</b>						
Q <sub>g</sub>	Total Gate Charge	V <sub>DD</sub> =50V, I <sub>D</sub> =10A, V <sub>GS</sub> =10V	--	26	--	nC
Q <sub>gs</sub>	Gate Source Charge		--	5.4	--	nC
Q <sub>gd</sub>	Gate Drain Charge		--	5.8	--	nC
t <sub>d(on)</sub>	Turn-on Delay Time	V <sub>DD</sub> =50V, I <sub>D</sub> =10A, V <sub>GS</sub> =10V, R <sub>G</sub> =3Ω	--	7	--	nS
t <sub>r</sub>	Turn-on Rise Time		--	24	--	nS
t <sub>d(off)</sub>	Turn-Off Delay Time		--	25	--	nS
t <sub>f</sub>	Turn-Off Fall Time		--	31	--	nS
<b>Source- Drain Diode Characteristics</b>						
V <sub>SD</sub>	Forward on voltage	T <sub>j</sub> =25°C, I <sub>S</sub> =10A	--	--	1.2	V

### Typical Operating Characteristics

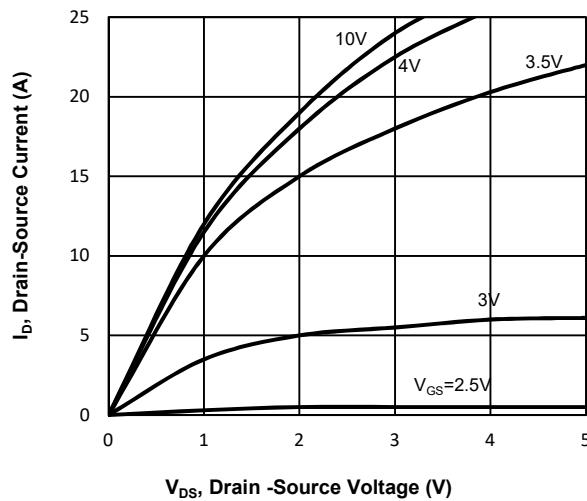


Fig1. Typical Output Characteristics

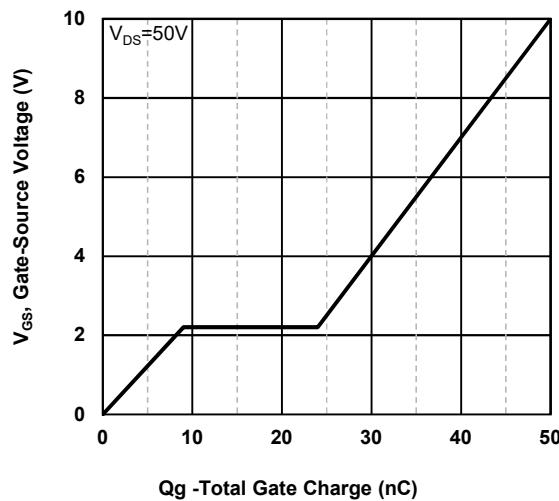


Fig2. Typical Gate Charge Vs.Gate-Source Voltage

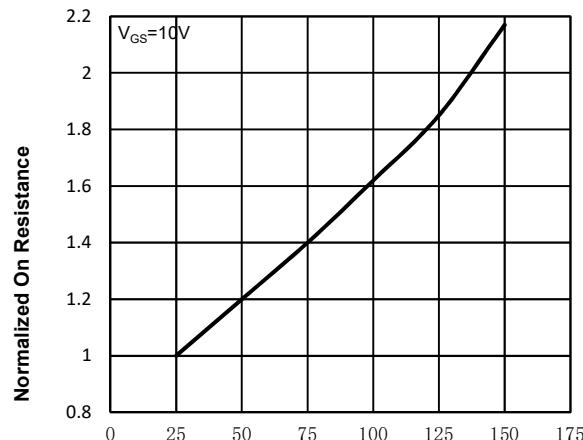


Fig3. Normalized On-Resistance Vs. Temperature

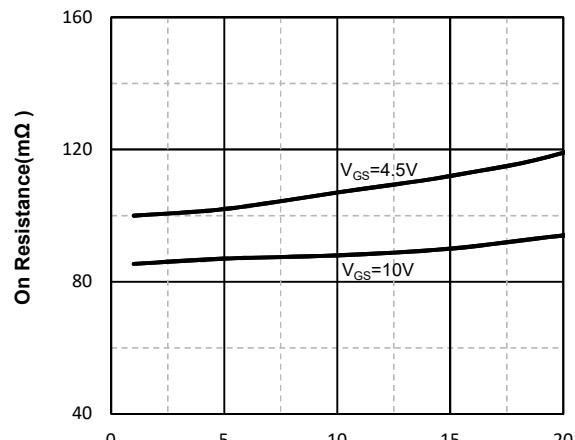


Fig4. On-Resistance Vs. Drain-Source Current

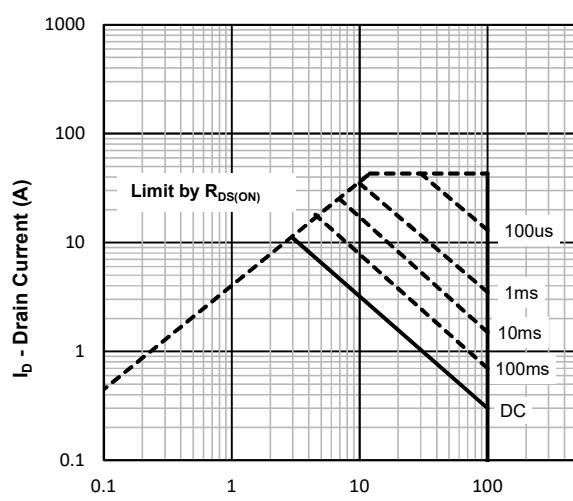


Fig5. Maximum Safe Operating Area

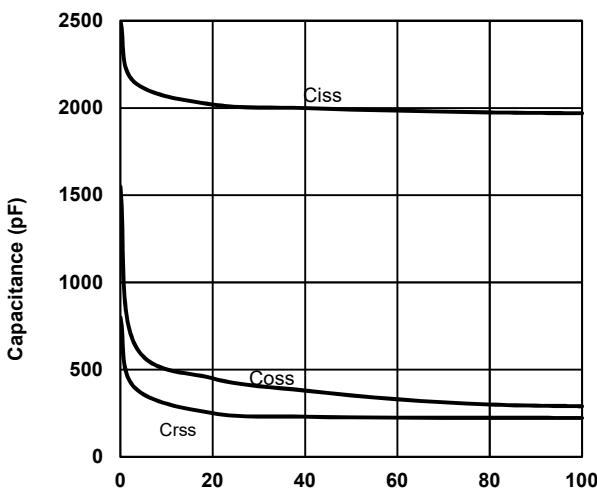
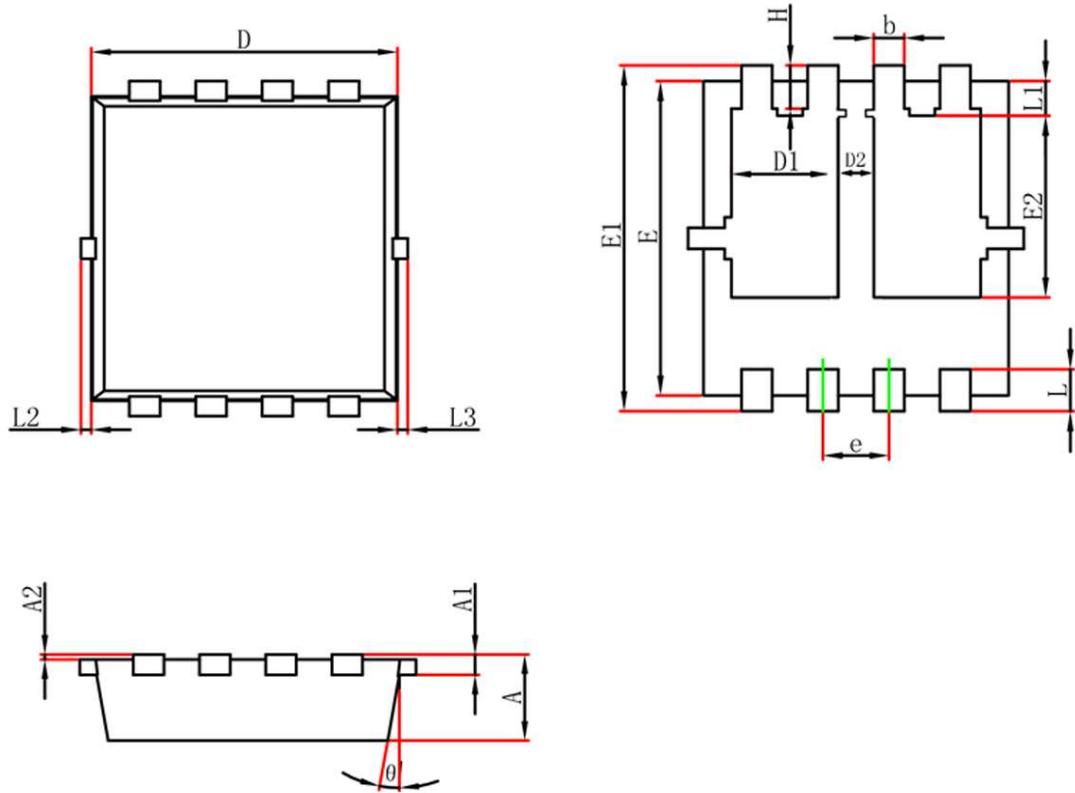


Fig6 Typical Capacitance Vs.Drain-Source Voltage



**PDFN3X3-8L Package information**



Symbol	Dimensions in Millimeters(mm)		Dimensions In Inches	
	Min	Max	Min	Max
A	0.750	0.850	0.030	0.034
A1	0.152 REF.		0.006 REF.	
A2	0~0.05		0~0.002	
D	3.050	3.150	0.121	0.125
D1	0.985	1.085	0.039	0.043
D2	0.330	0.430	0.013	0.017
E	2.950	3.050	0.117	0.121
E1	3.250	3.350	0.129	0.132
E2	1.685	1.785	0.067	0.071
b	0.250	0.350	0.010	0.014
e	0.600	0.700	0.024	0.028
L	0.350	0.450	0.014	0.018
L1	0.280	0.380	0.011	0.015
L2	0~0.100		0~0.004	
L3	0~0.100		0~0.004	
H	0.350	0.450	0.014	0.018
θ	9°	13°	10°	12°