

Features

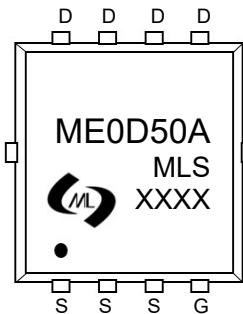
- High density cell design for ultra low $R_{DS(ON)}$
- Fully characterized avalanche voltage and current
- Excellent package for good heat dissipation

Product Summary

V_{DS}	$R_{DS(ON)} \text{ MAX}$	$I_D \text{ MAX}$
30V	6.5mΩ@10V	50A
	10mΩ@4.5V	

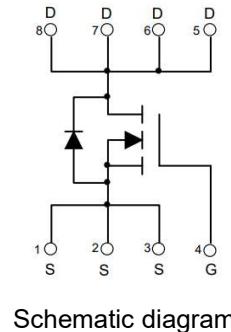
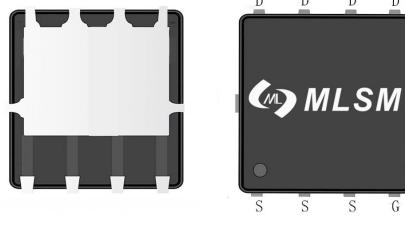
Application

- Power switching application



ME0D50A: Device code
XXXX: Code

Marking and pin assignment



Halogen-Free

Absolute Maximum Ratings (TA=25°C unless otherwise noted)

Symbol	Parameter	Rating	Unit
Common Ratings (TC=25°C Unless Otherwise Noted)			
V_{DS}	Drain-Source Breakdown Voltage	30	V
V_{GS}	Gate-Source Voltage	± 20	V
T_J	Maximum Junction Temperature	150	°C
T_{STG}	Storage Temperature Range	-50 to 155	°C
I_S	Diode Continuous Forward Current	50	A
Mounted on Large Heat Sink			
I_{DM}	Pulse Drain Current Tested	200	A
I_D	Continuous Drain Current	50	A
P_D	Maximum Power Dissipation	41	W
$R_{\theta JA}$	Thermal Resistance Junction-Ambient	50	°C/W

Ordering Information (Example)

Type	Package	Marking	Minimum Package(pcs)	Inner Box Quantity(pcs)	Outer Carton Quantity(pcs)	Delivery Mode
ME0D50A	PDFN3X3-8L	ME0D50A	5,000	10,000	70,000	13" reel

Electrical Characteristics (T_J=25°C unless otherwise noted)

Symbol	Parameter	Condition	Min	Typ	Max	Unit
Static Electrical Characteristics @ T_J = 25°C (unless otherwise stated)						
BV _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250μA	30	--	--	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =30V, V _{GS} =0V	--	--	1	μA
I _{GSS}	Gate-Body Leakage Current	V _{GS} =±20V, V _{DS} =0V	--	--	±100	nA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250μA	1.0	1.7	2.5	V
R _{DS(on)}	Drain-Source On-State Resistance	V _{GS} =10V, I _D =20A	--	4.7	6.5	mΩ
		V _{GS} =4.5V, I _D =15A	--	7.3	10	mΩ
Dynamic Electrical Characteristics @ T_J = 25°C (unless otherwise stated)						
C _{ISS}	Input Capacitance	V _{DS} =15V, V _{GS} =0V, f=1MHz	--	1045	--	pF
C _{OSS}	Output Capacitance		--	180	--	pF
C _{RSS}	Reverse Transfer Capacitance		--	155	--	pF
Switching Characteristics						
Q _g	Total Gate Charge	V _{DD} =15V, I _D =20A, V _{GS} =10V	--	24	--	nC
Q _{gs}	Gate Source Charge		--	5	--	nC
Q _{gd}	Gate Drain Charge		--	6	--	nC
t _{d(on)}	Turn-on Delay Time	V _{DD} =15V, I _D =20A, V _{GS} =10V, R _G =3Ω	--	11	--	nS
t _r	Turn-on Rise Time		--	55	--	nS
t _{d(off)}	Turn-Off Delay Time		--	27	--	nS
t _f	Turn-Off Fall Time		--	66	--	nS
Source-Drain Diode Characteristics						
V _{SD}	Forward on voltage	T _J =25°C, I _S =20A	--	--	1.2	V

Typical Operating Characteristics

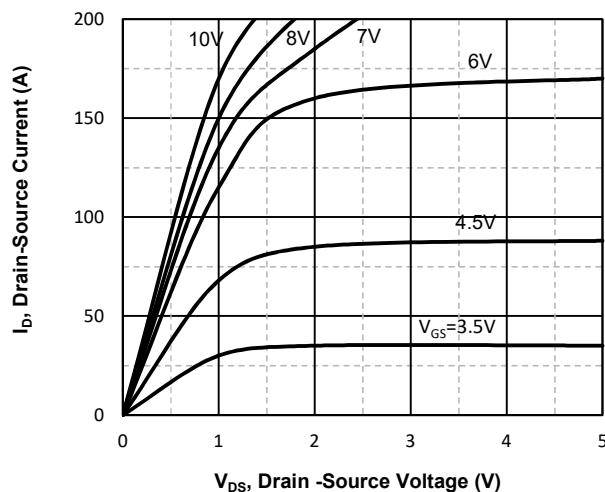


Fig1. Typical Output Characteristics

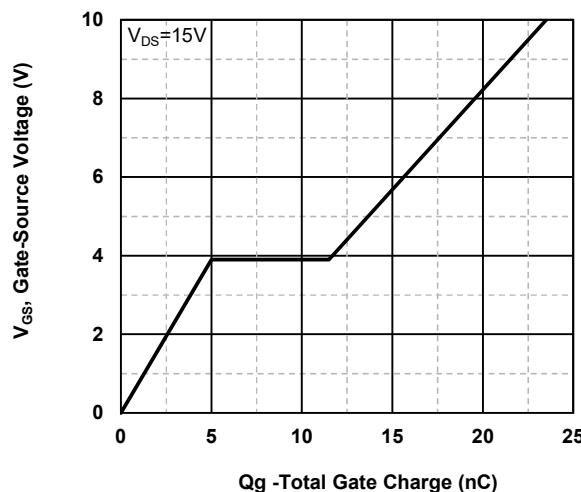


Fig2. Typical Gate Charge Vs. Gate-Source Voltage

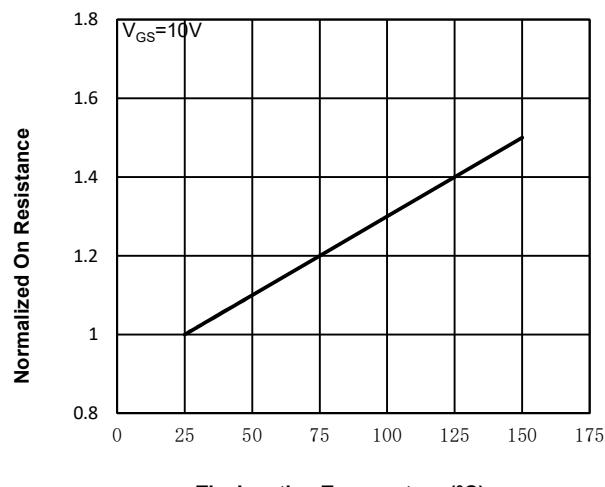


Fig3. Normalized On-Resistance Vs. Temperature

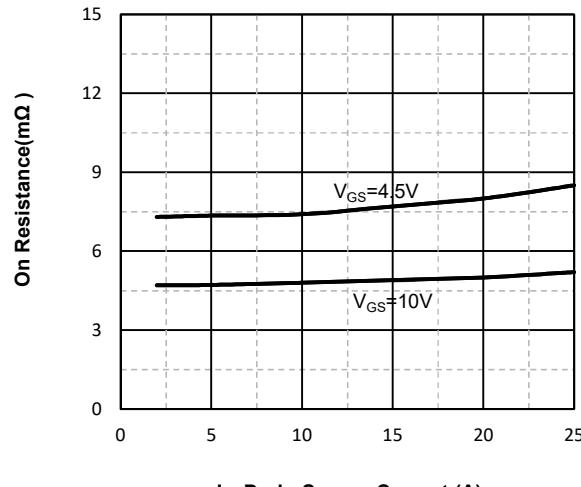


Fig4. On-Resistance Vs. Drain-Source Current

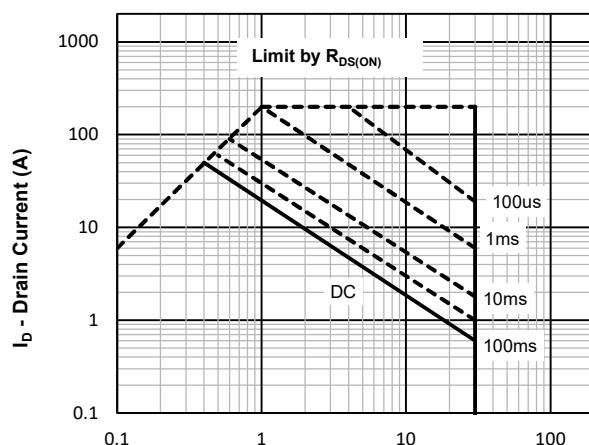


Fig5. Maximum Safe Operating Area

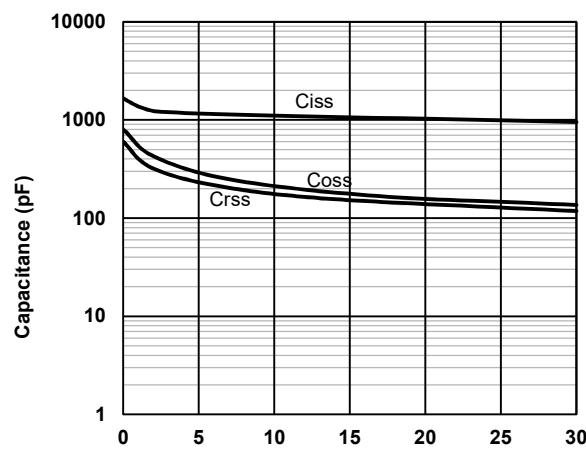
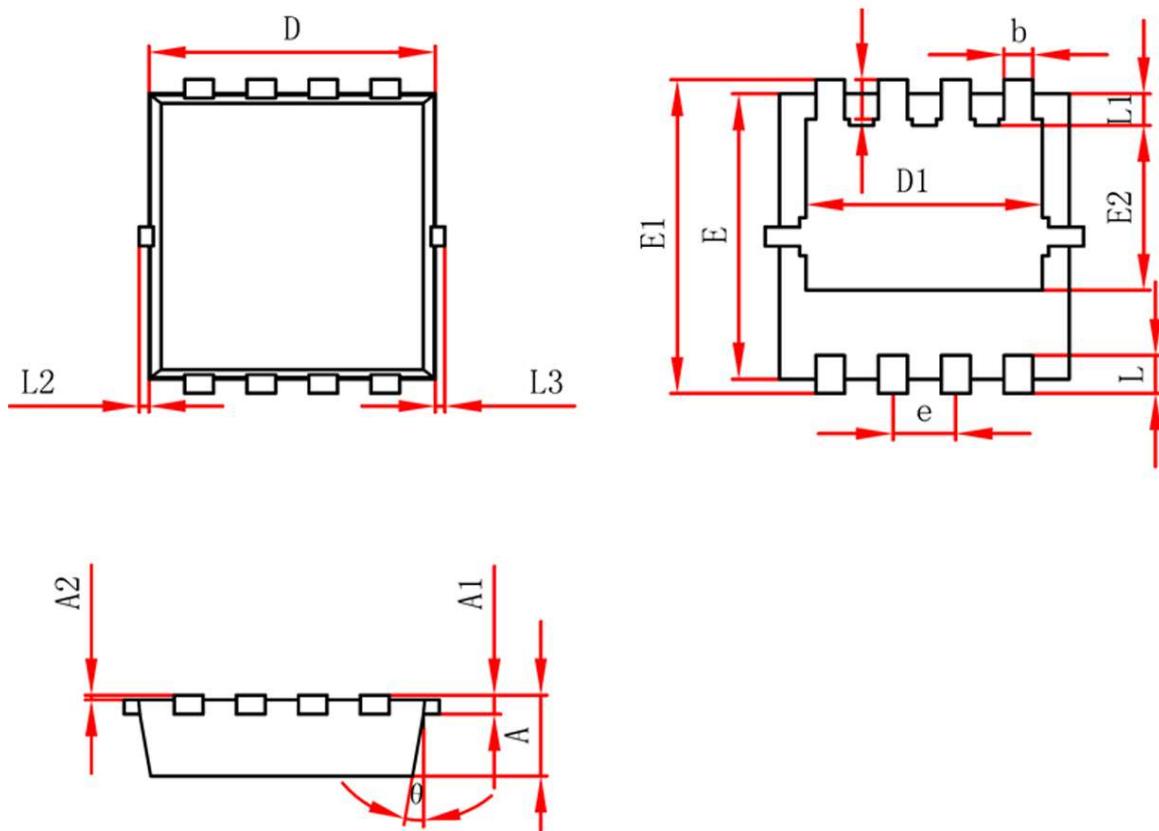


Fig6 Typical Capacitance Vs.Drain-Source

PDFN3X3-8L Package information



Symbol	Dimensions in Millimeters(mm)		Dimensions In Inches	
	Min	Max	Min	Max
A	0.750	0.850	0.030	0.034
A1	0.152 REF.		0.006 REF.	
A2	0~0.05		0~0.002	
D	2.950	3.150	0.117	0.125
D1	2.400	2.500	0.095	0.099
E	2.950	3.050	0.117	0.121
E1	3.250	3.350	0.129	0.132
E2	1.685	1.785	0.067	0.071
b	0.250	0.350	0.010	0.014
e	0.600	0.700	0.024	0.028
L	0.350	0.450	0.014	0.018
L1	0.325	0.425	0.013	0.017
L2	0~0.100		0~0.004	
L3	0~0.100		0~0.004	
H	0.365	0.465	0.014	0.018
θ	10°	12°	10°	12°