

Features

- Super high density cell design for extremely low $R_{DS(ON)}$
- Exceptional on-resistance and maximum DC current capability

Product Summary

V_{DS}	$R_{DS(ON)}$ TYP	I_D
100V	65mΩ@10V	15A
	75mΩ@4.5V	

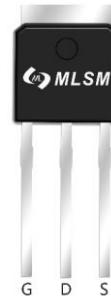
Application

- Power Management in Note book
- DC/DC Converter
- Load Switch
- LCD Display inverter

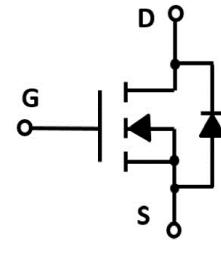


MD1A15A : Device code
xxxx : Code

Marking and pin assignment



TO-251 top view



Schematic diagram



Halogen-Free

Absolute Maximum Ratings (TA=25°C unless otherwise noted)

Symbol	Parameter	Rating	Unit
Common Ratings (TC=25°C Unless Otherwise Noted)			
V_{DS}	Drain-Source Breakdown Voltage	100	V
V_{GS}	Gate-Source Voltage	± 20	V
T_J	Maximum Junction Temperature	150	°C
T_{STG}	Storage Temperature Range	-50 to 155	°C
I_S	Diode Continuous Forward Current	15	A

Mounted on Large Heat Sink

I_{DM}	Pulse Drain Current Tested	60	A
I_D	Continuous Drain Current	15	A
P_D	Maximum Power Dissipation	50	W
EAS	Single pulse Avalanche Energy	18	mJ

Ordering Information (Example)

Type	Package	Marking	Minimum Package(pcs)	Inner Box Quantity(pcs)	Outer Carton Quantity(pcs)
MD1A15A	TO-251	MD1A15A	80	4,000	20,000



Electrical Characteristics (T_J=25°C unless otherwise noted)

Symbol	Parameter	Condition	Min	Typ	Max	Unit
Static Electrical Characteristics @ T_J = 25°C (unless otherwise stated)						
BV _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250μA	100	--	--	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =100V, V _{GS} =0V	--	--	1	μA
I _{GSS}	Gate-Body Leakage Current	V _{GS} =±20V, V _{DS} =0V	--	--	±100	nA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250μA	1	2	3	V
R _{DS(on)}	Drain-Source On-State Resistance	V _{GS} =10V, I _D =10A	--	65	80	mΩ
		V _{GS} =4.5V, I _D =8A	--	75	100	mΩ
Dynamic Electrical Characteristics @ T_J = 25°C (unless otherwise stated)						
C _{ISS}	Input Capacitance	V _{DS} =50V, V _{GS} =0V, f=1MHz	--	830	--	pF
C _{OSS}	Output Capacitance		--	44	--	pF
C _{RSS}	Reverse Transfer Capacitance		--	30	--	pF
Switching Characteristics						
Q _g	Total Gate Charge	V _{DD} =50V, I _D =10A, V _{GS} =10V	--	22	--	nC
Q _{gs}	Gate Source Charge		--	2.9	--	nC
Q _{gd}	Gate Drain Charge		--	6.2	--	nC
t _{d(on)}	Turn-on Delay Time	V _{DD} =50V, R _L =6.4Ω, V _{GS} =10V, R _G =3Ω	--	25	--	nS
t _r	Turn-on Rise Time		--	430	--	nS
t _{d(off)}	Turn-Off Delay Time		--	45	--	nS
t _f	Turn-Off Fall Time		--	92	--	nS
Source- Drain Diode Characteristics						
V _{SD}	Forward on voltage	T _j =25°C, I _S =15A,	--	--	1.2	V

Typical Operating Characteristics

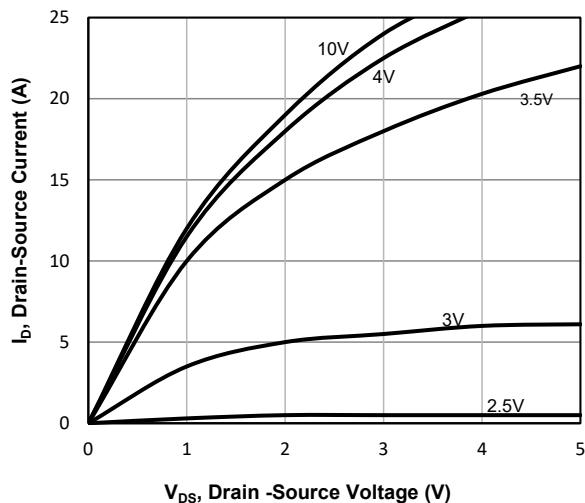


Fig1. Typical Output Characteristics

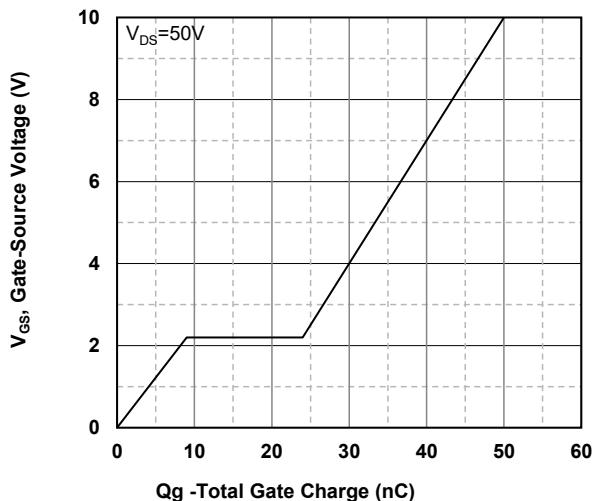


Fig2. Typical Gate Charge Vs.Gate-Source Voltage

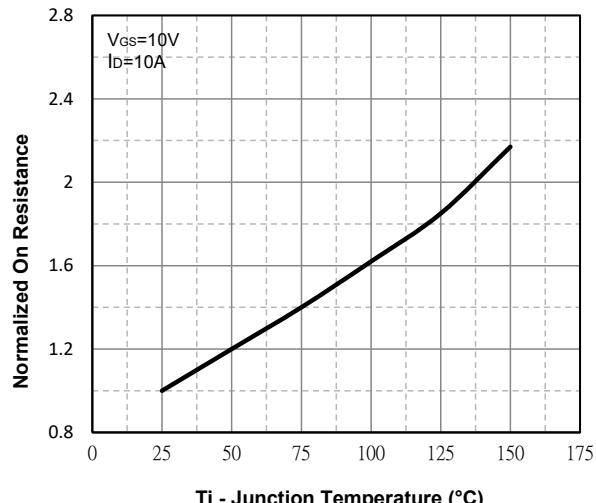


Fig3. Normalized On-Resistance Vs. Temperature

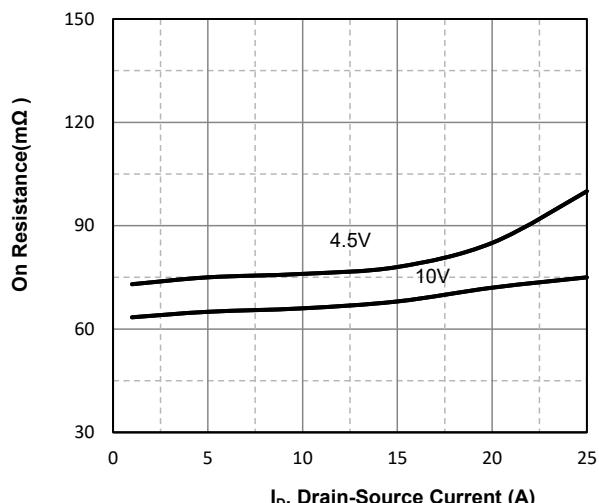


Fig4. On-Resistance Vs. Drain-Source Current

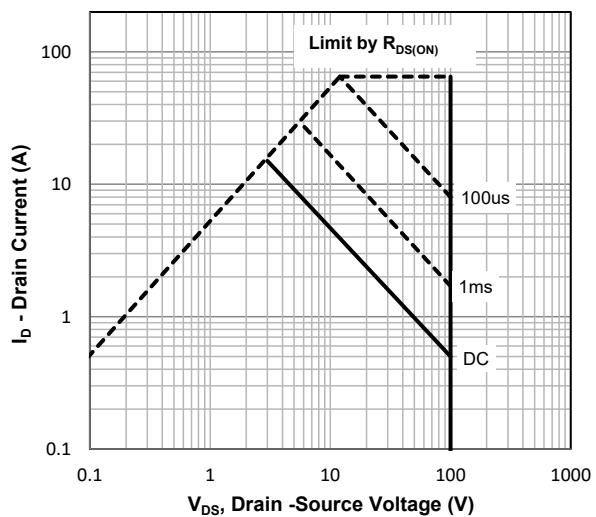


Fig5. Maximum Safe Operating Area

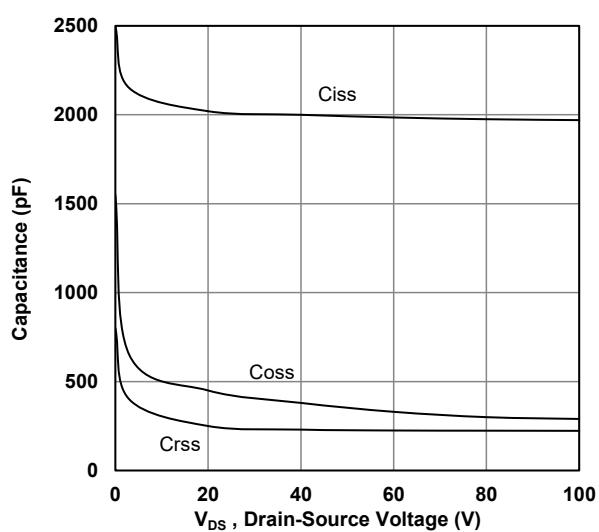
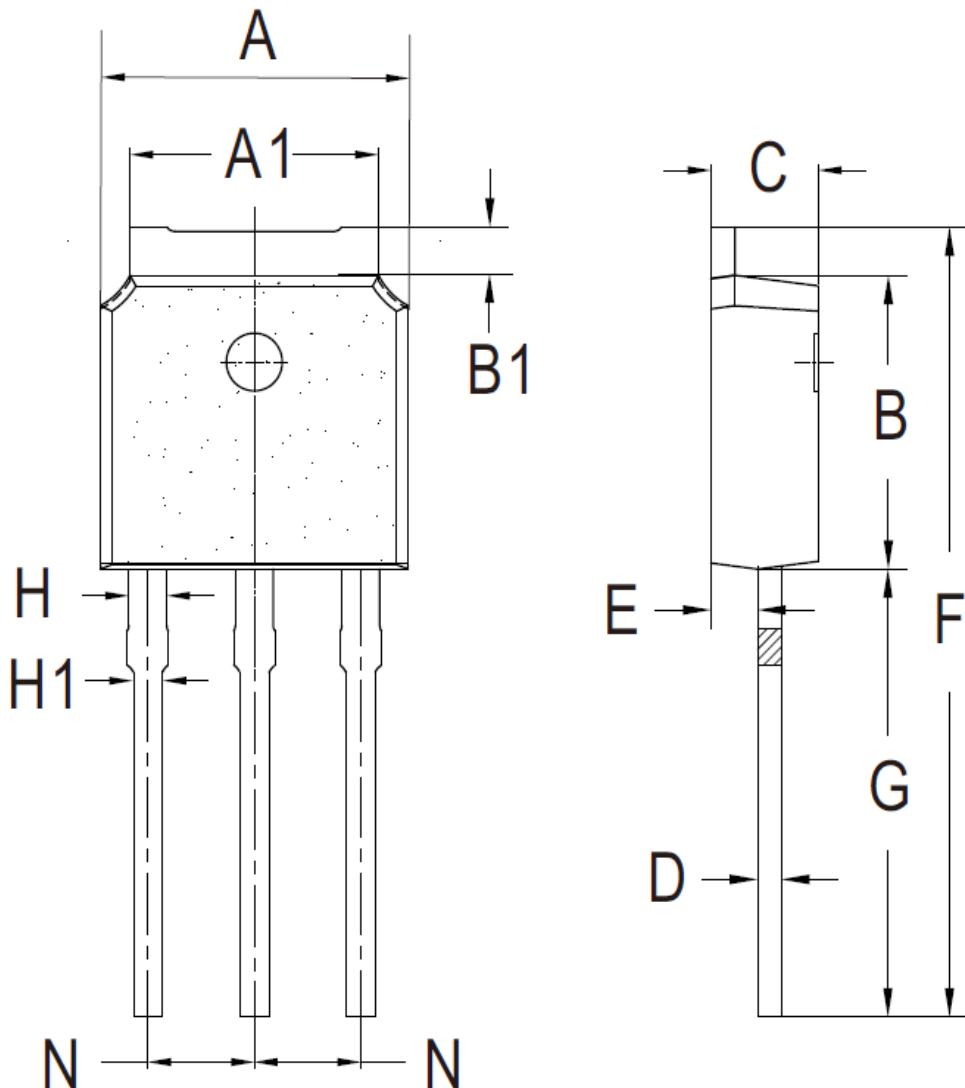


Fig6 Typical Capacitance Vs.Drain-Source Voltage

TO-251 Package information



Symbol	Dimensions in Millimeters(mm)		Dimensions In Inches	
	Min	Max	Min	Max
A	6.400	6.800	0.253	0.269
A1	5.200	5.500	0.206	0.217
B	5.900	6.300	0.233	0.249
B1	0.950	1.250	0.038	0.049
C	2.100	2.500	0.083	0.099
D	0.400	0.600	0.016	0.024
E	0.900	1.100	0.036	0.043
F	16.100	16.700	0.637	0.660
G	9.200	9.600	0.364	0.380
H	0.700	0.900	0.028	0.036
H1	0.500	0.700	0.020	0.028
N	2.200	2.400	0.087	0.095