

Features

- High power and current handing capability
 - Lead free product is acquired
 - Surface mount package

Application

- Battery protection
 - Load switch
 - Power management

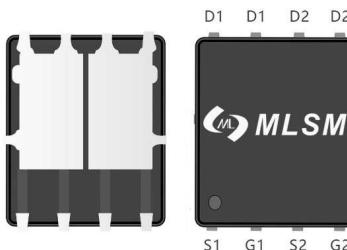
A top-down diagram of a MCPE25A integrated circuit package. It is a rectangular chip with a gold-plated metal lead frame. The package has four pins labeled D1, D1, D2, and D2 at the top edge. In the center, the part number "MCPE25A" is printed above the letters "MLS". Below "MLS" is a stylized "M" logo consisting of two curved lines forming a loop. At the bottom center is a small black dot. The package is shown from a perspective view, with the top edge slightly recessed.

MCPE25A : Device code
XXXX : Code

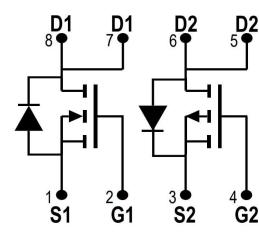
Marking and pin assignment

Product Summary

V_{DS}	$R_{DS(ON)}\text{ MAX}$	$I_D\text{ MAX}$
40V	25m Ω @10V	25A
	35m Ω @4.5V	
-40V	40m Ω @-10V	-25A
	55m Ω @-4.5V	



PDFN5X6-8L view



Schematic diagram



The image contains two logos: a circular 'Pb-Free' logo with a crossed-out lead symbol, and a green leaf icon next to the text 'RoHS'.



Halogen-Free

Absolute Maximum Ratings (TA=25°C unless otherwise noted)

Symbol	Parameter	N-Channel	P-Channel	Unit	
Common Ratings (TC=25°C Unless Otherwise Noted)					
V _{DS}	Drain-Source Breakdown Voltage	40	-40	V	
V _{GS}	Gate-Source Voltage	±20	±20	V	
T _J	Maximum Junction Temperature	150	150	°C	
T _{STG}	Storage Temperature Range	-55 to 150	-55 to 150	°C	
I _S	Diode Continuous Forward Current	Tc=25°C	25	-25	A
Mounted on Large Heat Sink					
I _{DM}	Pulse Drain Current Tested	Tc=25°C	100	-100	A
I _D	Continuous Drain Current	Tc=25°C	25	-25	A
P _D	Maximum Power Dissipation	Tc=25°C	35	35	W
R _{θJA}	Thermal Resistance Junction-Ambient		83	83	°C/W

Ordering Information (Example)

Type	Package	Marking	Minimum Package(pcs)	Inner Box Quantity(pcs)	Outer Carton Quantity(pcs)	Delivery Mode
MCPE25A	PDFN5X6-8L	MCPE25A	5,000	10,000	70,000	13"reel

N-Ch Electrical Characteristics (TJ=25°C unless otherwise noted)						
Symbol	Parameter	Condition	Min	Typ	Max	Unit
Static Electrical Characteristics @ TJ = 25°C (unless otherwise stated)						
BV _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250μA	40	--	--	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =40V, V _{GS} =0V	--	--	1	μA
I _{GSS}	Gate-Body Leakage Current	V _{GS} =±20V, V _{DS} =0V	--	--	±100	nA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250μA	1.0	1.4	2.5	V
R _{DS(on)}	Drain-Source On-State Resistance	V _{GS} =10V, I _D =20A	--	20	25	mΩ
		V _{GS} =4.5V, I _D =10A	--	25	35	mΩ
Dynamic Electrical Characteristics @ TJ = 25°C (unless otherwise stated)						
C _{ISS}	Input Capacitance	V _{DS} =20V, V _{GS} =0V, f=1MHz	--	400	--	pF
C _{OSS}	Output Capacitance		--	120	--	pF
C _{RSS}	Reverse Transfer Capacitance		--	15	--	pF
Switching Characteristics						
Q _g	Total Gate Charge	V _{DS} =20V, I _D =10A, V _{GS} =10V	--	10.3	--	nC
Q _{gs}	Gate Source Charge		--	1.8	--	nC
Q _{gd}	Gate Drain Charge		--	2.3	--	nC
t _{d(on)}	Turn-on Delay Time	V _{DD} =20V, R _L =2.5Ω, V _{GS} =10V, R _G =3Ω	--	4	--	nS
t _r	Turn-on Rise Time		--	18	--	nS
t _{d(off)}	Turn-Off Delay Time		--	14	--	nS
t _f	Turn-Off Fall Time		--	20	--	nS
Source- Drain Diode Characteristics						
V _{SD}	Forward on voltage	T _j =25°C, I _S =10A	--	--	1.2	V

P-CH Electrical Characteristics (TJ=25°C unless otherwise noted)						
Symbol	Parameter	Condition	Min	Typ	Max	Unit
Static Electrical Characteristics @ TJ = 25°C (unless otherwise stated)						
BV _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =-250μA	-40	--	--	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =-40V, V _{GS} =0V	--	--	-1	μA
I _{GSS}	Gate-Body Leakage Current	V _{GS} =±20V, V _{DS} =0V	--	--	±100	nA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =-250μA	-1.0	-1.5	-2.5	V
R _{DS(on)}	Drain-Source On-State Resistance	V _{GS} =-10V, I _D =-20A	--	30	40	mΩ
		V _{GS} =-4.5V, I _D =-10A	--	40	55	mΩ
Dynamic Electrical Characteristics @ TJ = 25°C (unless otherwise stated)						
C _{ISS}	Input Capacitance	V _{DS} =-20V, V _{GS} =0V, f=1MHz	--	1150	--	pF
C _{OSS}	Output Capacitance		--	285	--	pF
C _{RSS}	Reverse Transfer Capacitance		--	195	--	pF
Switching Characteristics						
Q _g	Total Gate Charge	V _{DD} =-20V, I _D =-10A, V _{GS} =-10V	--	18.8	--	nC
Q _{gs}	Gate Source Charge		--	8.2	--	nC
Q _{gd}	Gate Drain Charge		--	10.9	--	nC
t _{d(on)}	Turn-on Delay Time	V _{DD} =-20V, I _D =-10A, V _{GS} =-10V, R _G =2.5Ω	--	12	--	nS
t _r	Turn-on Rise Time		--	18	--	nS
t _{d(off)}	Turn-Off Delay Time		--	35	--	nS
t _f	Turn-Off Fall Time		--	26	--	nS
Source- Drain Diode Characteristics						
V _{SD}	Forward on voltage	T _j =25°C, I _S =-10A	--	--	-1.2	V

N-Channel Typical Operating Characteristics

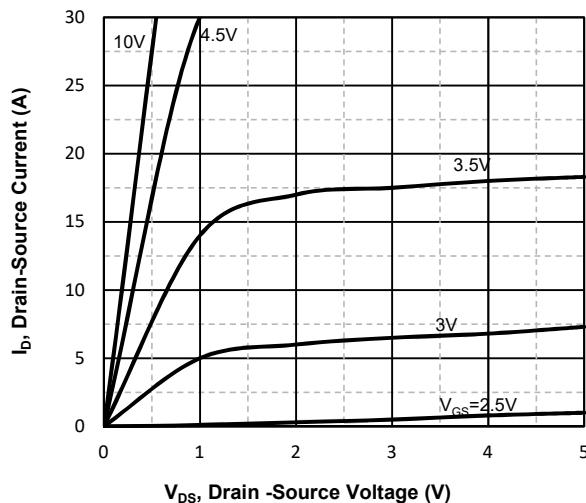


Fig1. Typical Output Characteristics

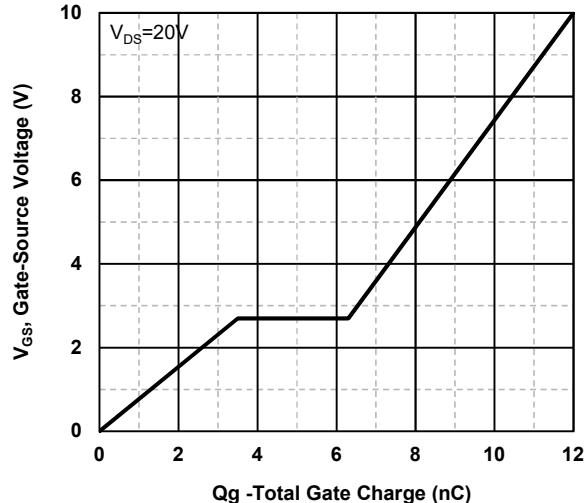


Fig2. Typical Gate Charge Vs. Gate-Source Voltage

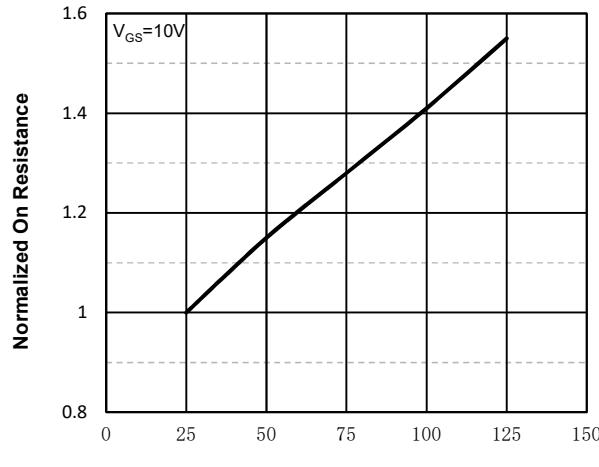


Fig3. Normalized On-Resistance Vs. Temperature

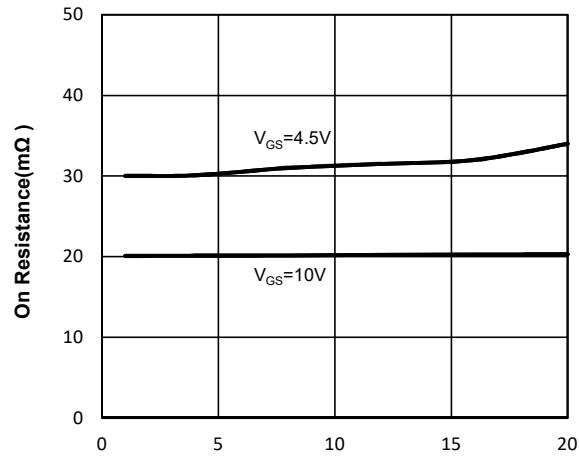


Fig4. On-Resistance Vs. Drain-Source Current

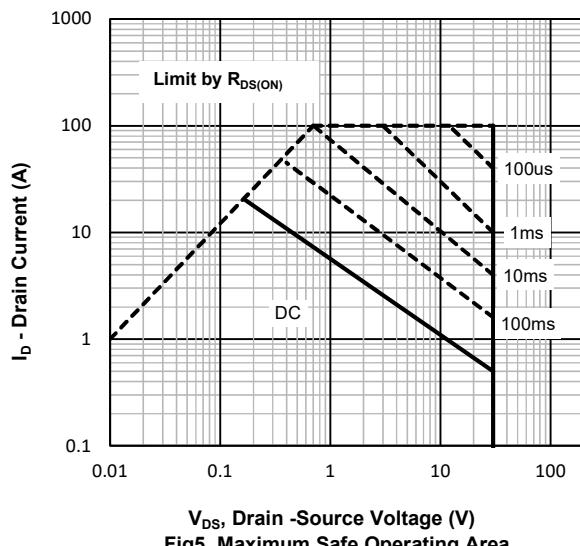


Fig5. Maximum Safe Operating Area

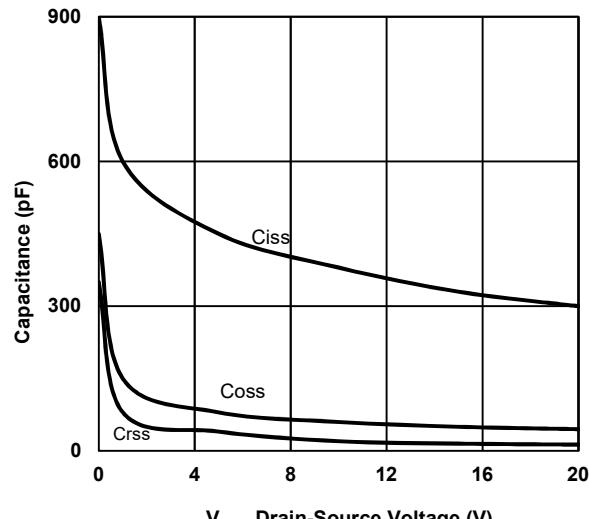


Fig6. Typical Capacitance Vs. Drain-Source Voltage

P-Channel Typical Operating Characteristics

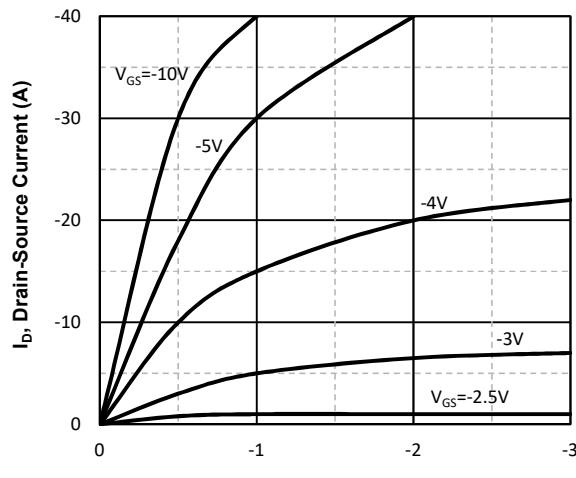


Fig7. Typical Output Characteristics

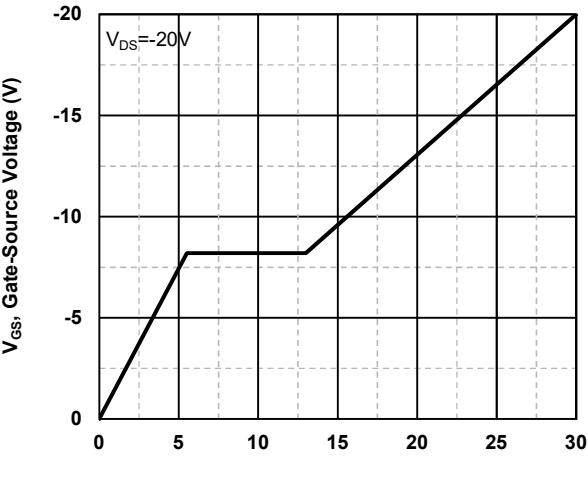


Fig8. Typical Gate Charge Vs. Gate-Source Voltage

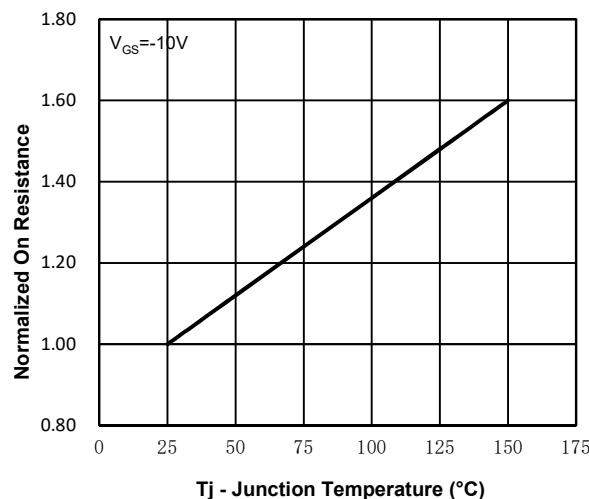


Fig9. Normalized On-Resistance Vs. Temperature

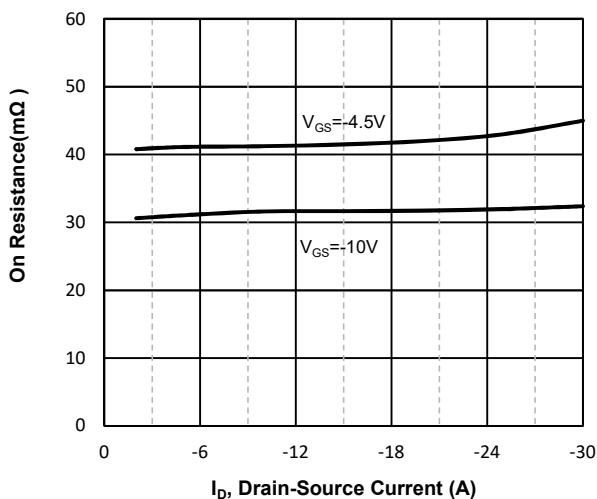


Fig10. On-Resistance Vs. Drain-Source Current

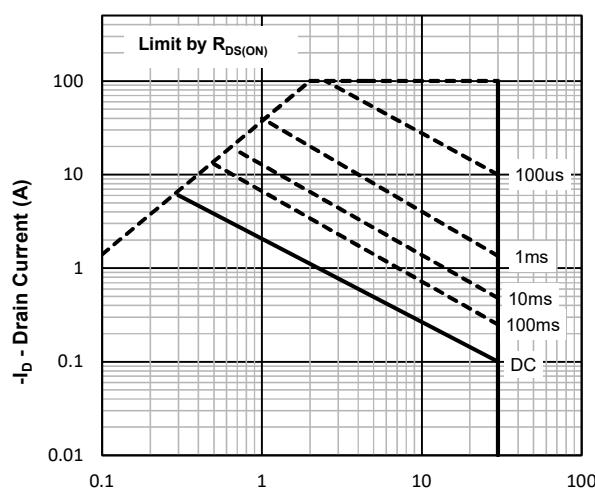


Fig11. Maximum Safe Operating Area

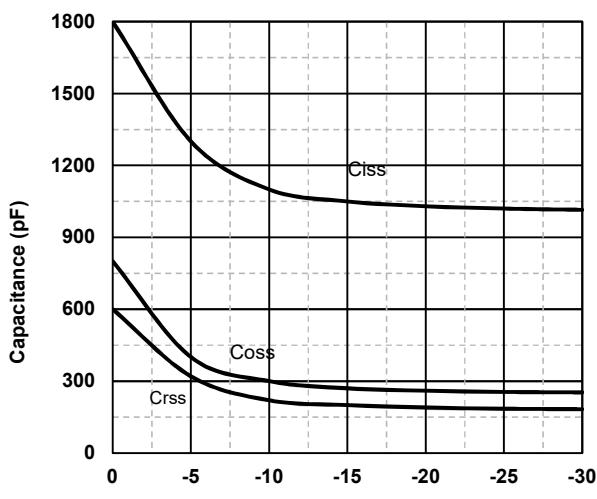
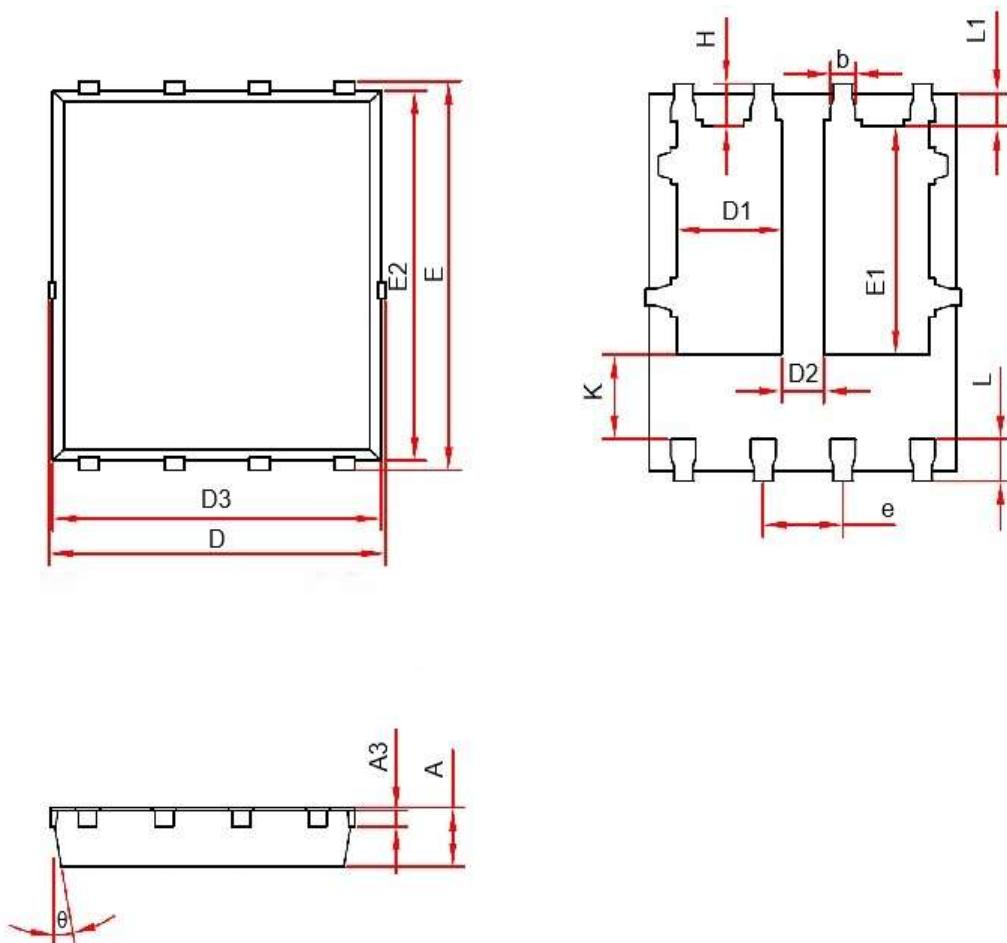


Fig12. Typical Capacitance Vs. Drain-Source Voltage

PDFN5X6-8L Package information


Symbol	Dimensions in Millimeters(mm)		Dimensions In Inches	
	Min	Max	Min	Max
A	0.950	1.050	0.035	0.039
A3	0.254REF.		0.010REF.	
D	4.950	5.050	0.196	0.200
E	5.950	6.050	0.235	0.239
D1	1.470	1.870	0.058	0.074
D2	0.470	0.870	0.019	0.034
E1	3.510	3.610	0.139	0.143
D3	4.850	4.950	0.192	0.196
E2	5.700	5.800	0.225	0.229
k	1.190	1.390	0.047	0.055
b	0.300	0.400	0.012	0.016
e	1.270TYP.		0.050TYP.	
L	0.559	0.711	0.022	0.028
L1	0.424	0.576	0.017	0.023
H	0.574	0.726	0.023	0.029
θ	10°	12°	10°	12°